

8

7

6

5

4

3

2

1

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RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

* SEE ASSEMBLY INSTRUCTIONS

REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	30JUL18	R.MYERS
B	CHANGES AS PER ECR-091091	06NOV19	R.MYERS
C	CHANGES AS PER ECR-094346	30MAY20	J.BOWNE

TEMPLATE ENGINEER
R.MARION

DATE
30JUL18

HARDWARE SERVICES
-

HARDWARE SYSTEMS
-

TEST ENGINEER
T.KARPUS

COMPONENT ENGINEER
A.CAUILAN

TEST PROCESS
-

HARDWARE RELEASE
K.JABATAN

DESIGNER
V.VADUVA

PTD ENGINEER
R.MYERS

CHECKER
-

DATE
30JUL18

DATE
30JUL18

DATE
30JUL18

DATE
30JUL18

DATE
30JUL18

DATE
30JUL18

DATE
30JUL18

SCHEMATIC

HW TYPE : Customer Evaluation
Product(s) : ADRV9025,ADRV9026,ADRV9029
: ADRV9026-HB
PACKAGE : N/A-lead N/A N/A-family
: Pitch-pitch StyleVendor Style

MASTER PROJECT TEMPLATE
TBD

TESTER TEMPLATE
no_template

DRAWING NO.
02_049168-01

REV.
C

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

TOLERANCES

DECIMALS
X.XX +0.010
X.XXX +0.005

FRACTIONS
+1/32

ANGLES
+2

SIZE
D

SCALE
1:1

CODE ID NO.
CodeID

SHEET 1 OF 10

8

7

6

5

4

3

2

1

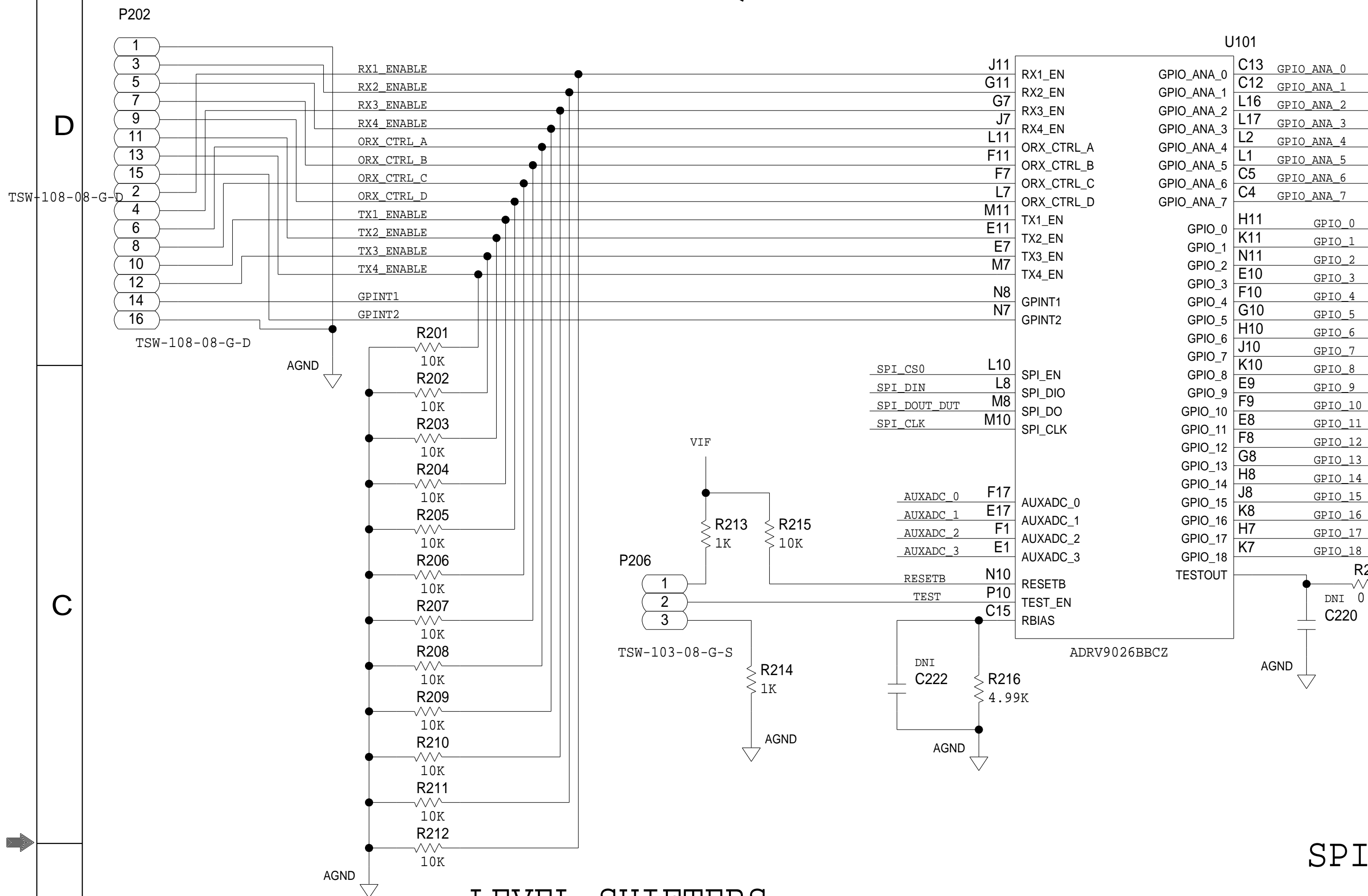
A

B

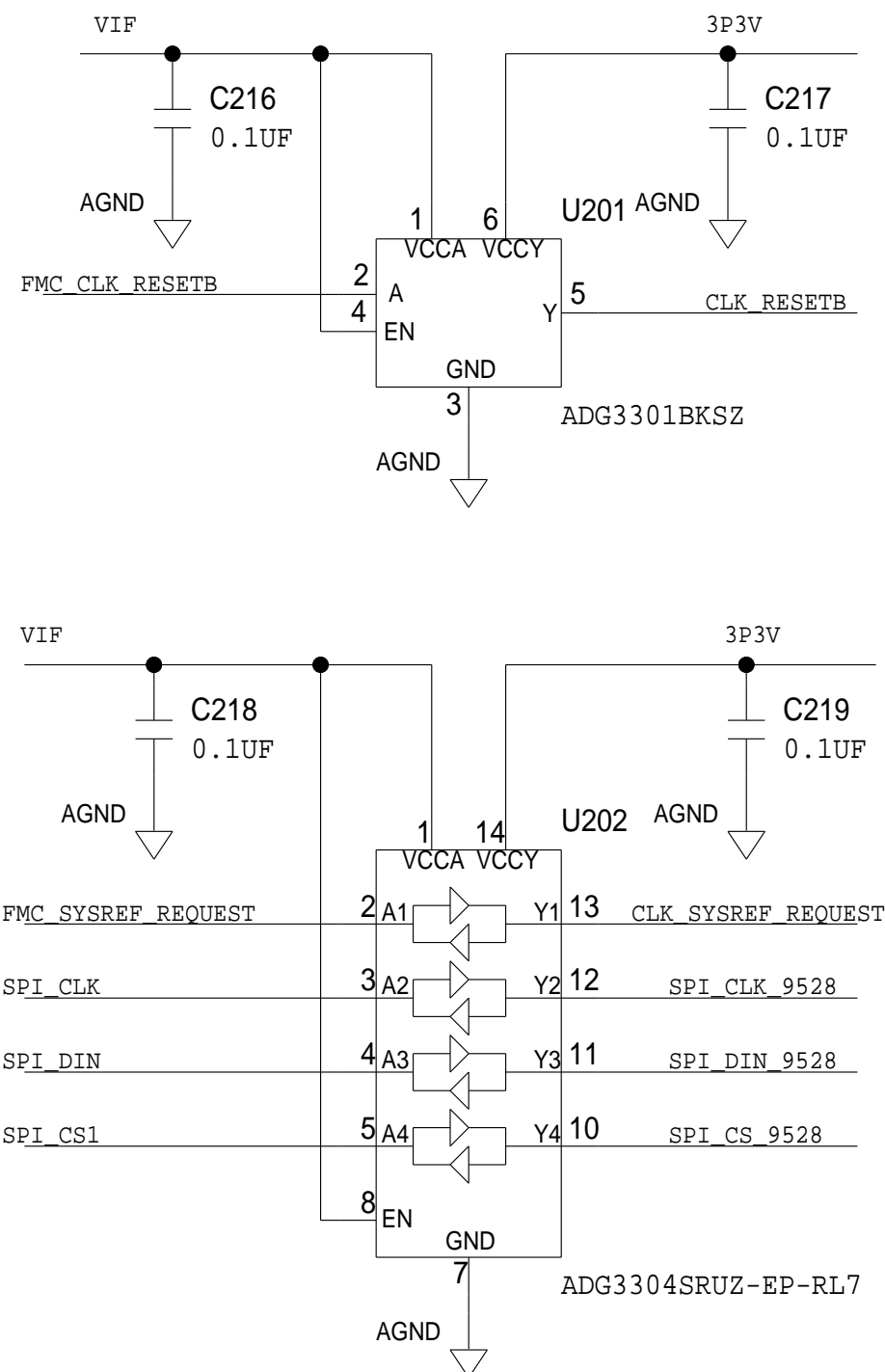
C

D

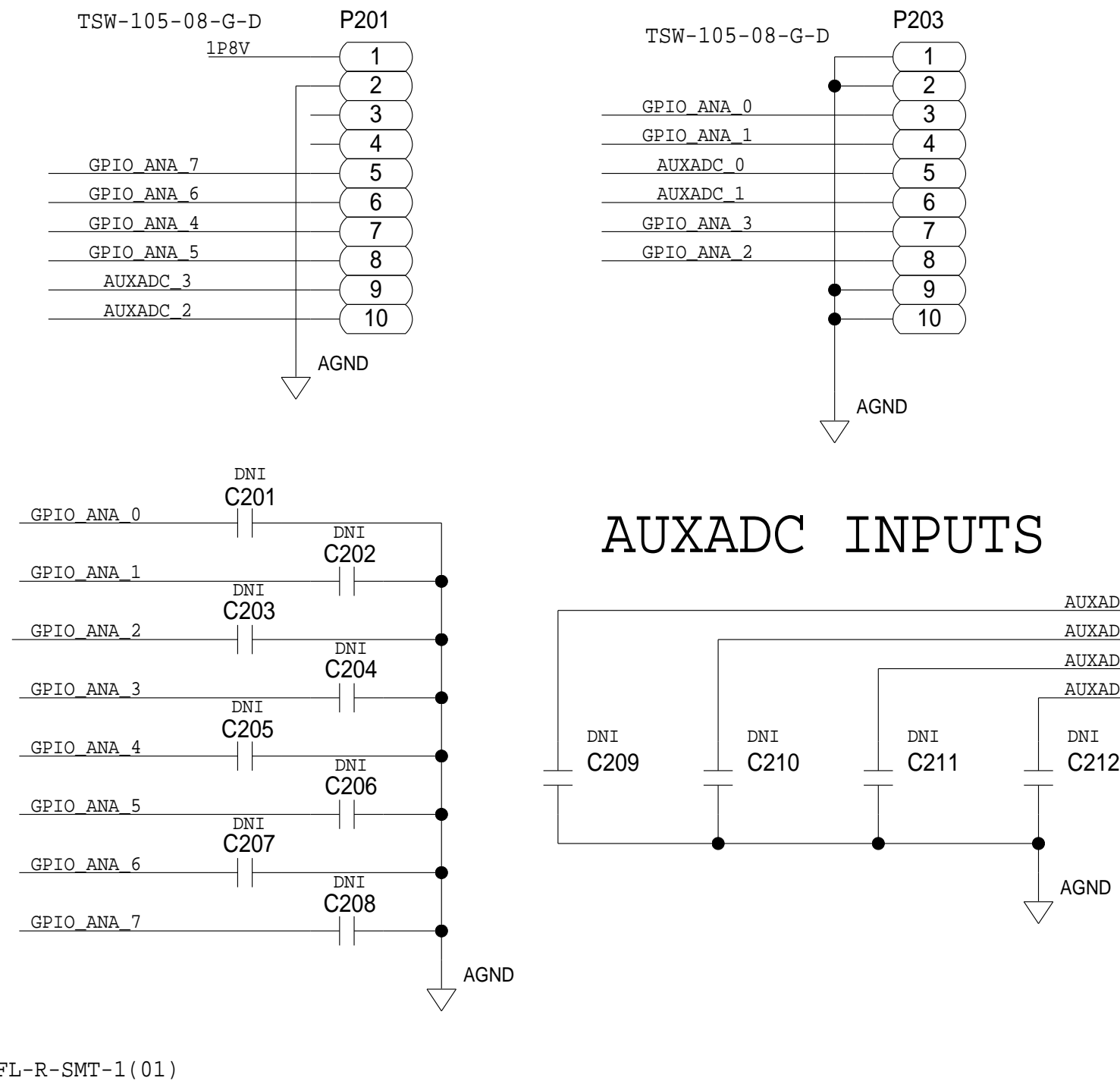
DIGITAL I/O



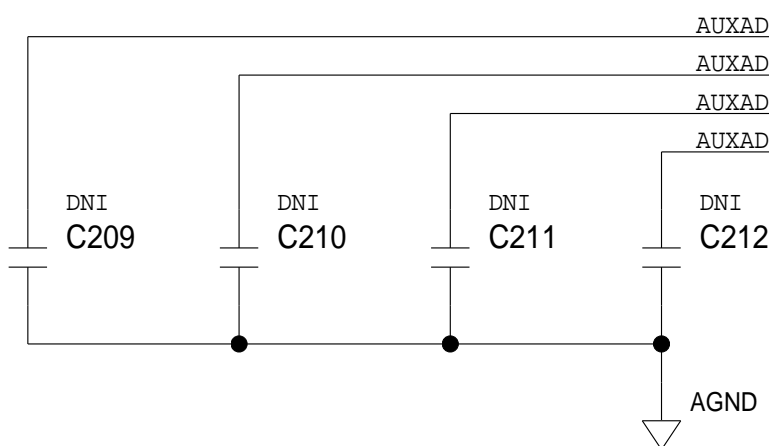
LEVEL SHIFTERS



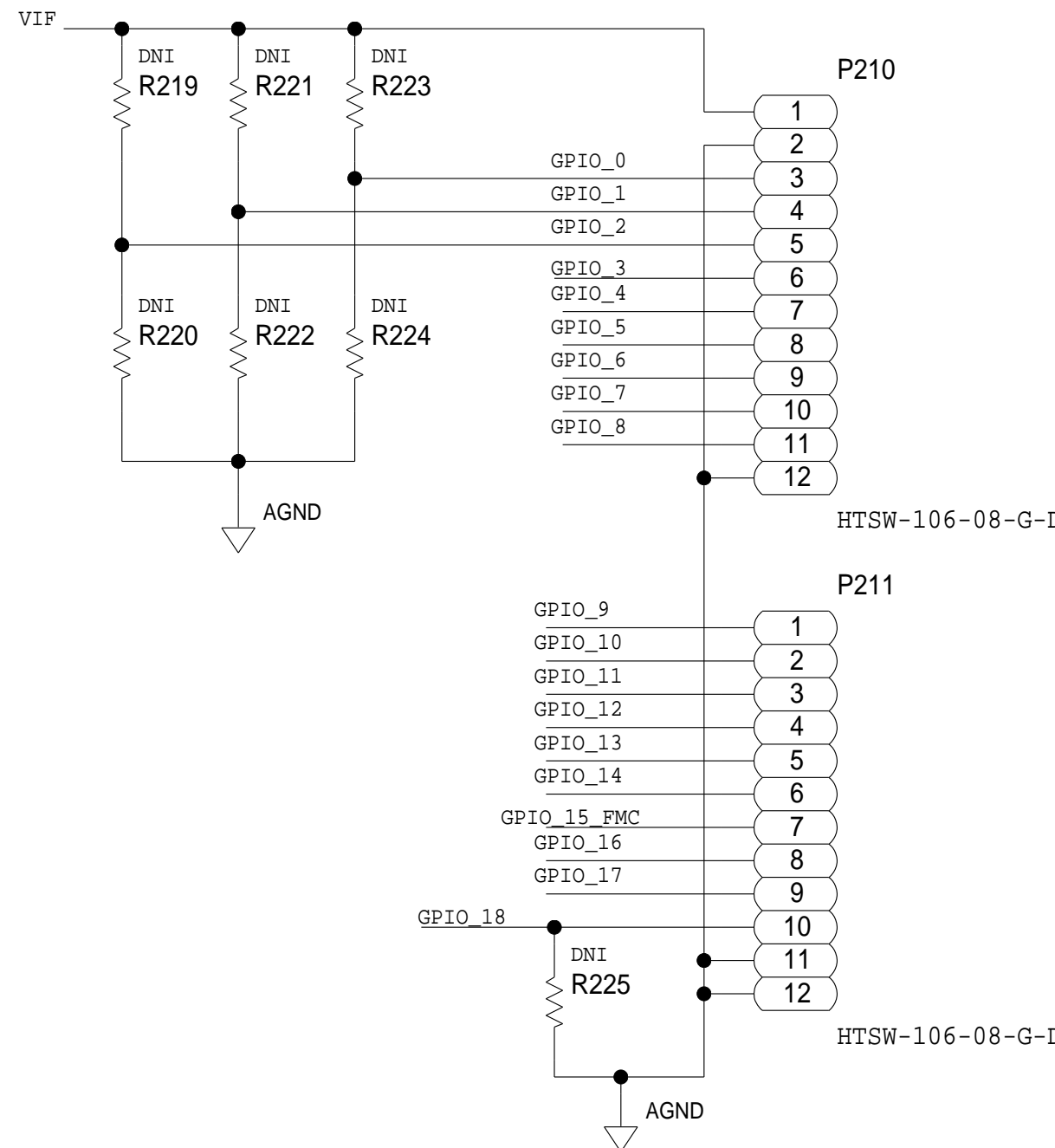
AUXDAC OUTPUTS AND 1.8V GPIO



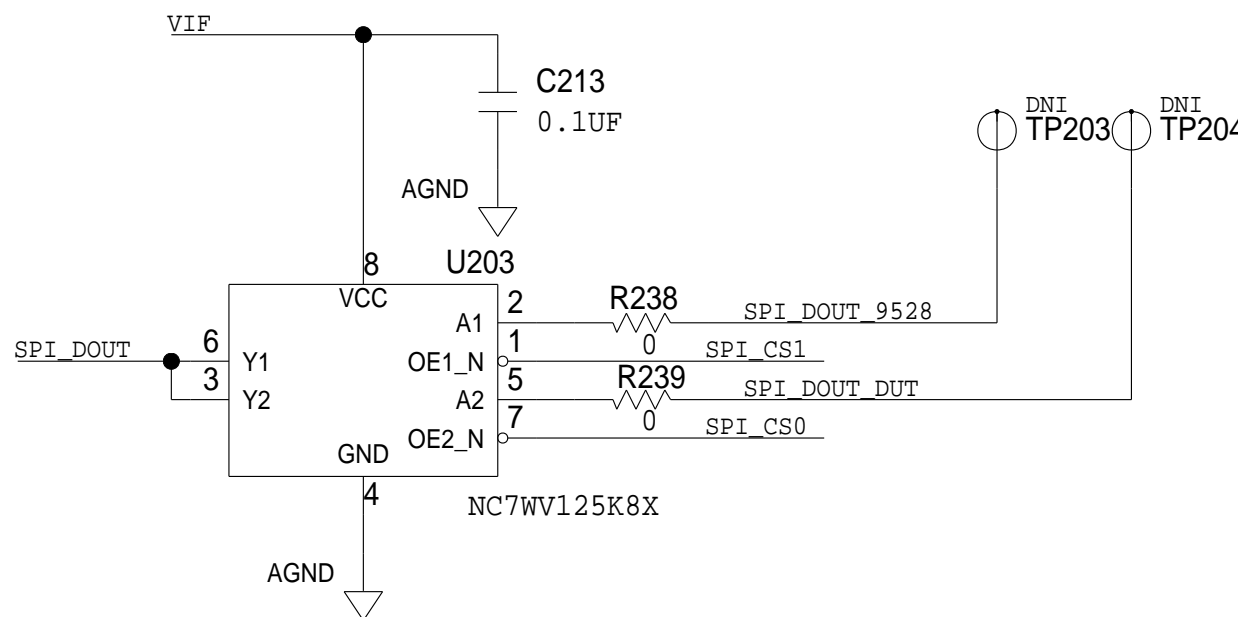
AUXADC INPUTS



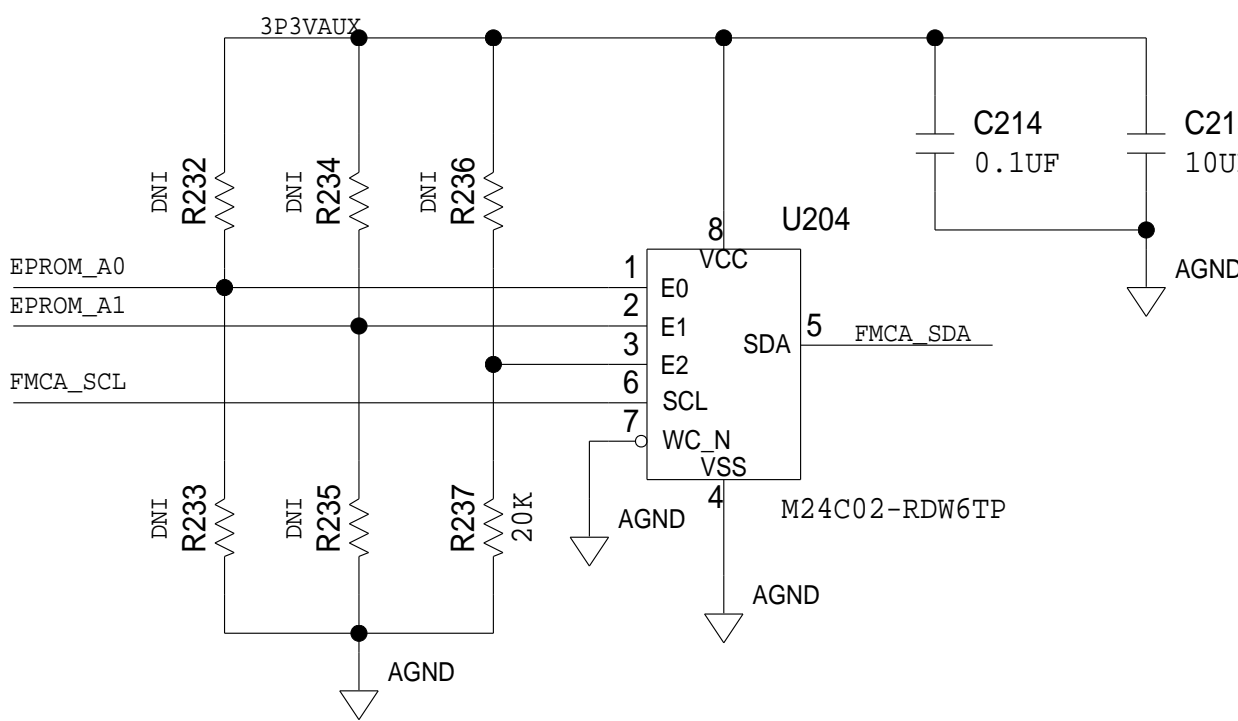
DIGITAL GPIO



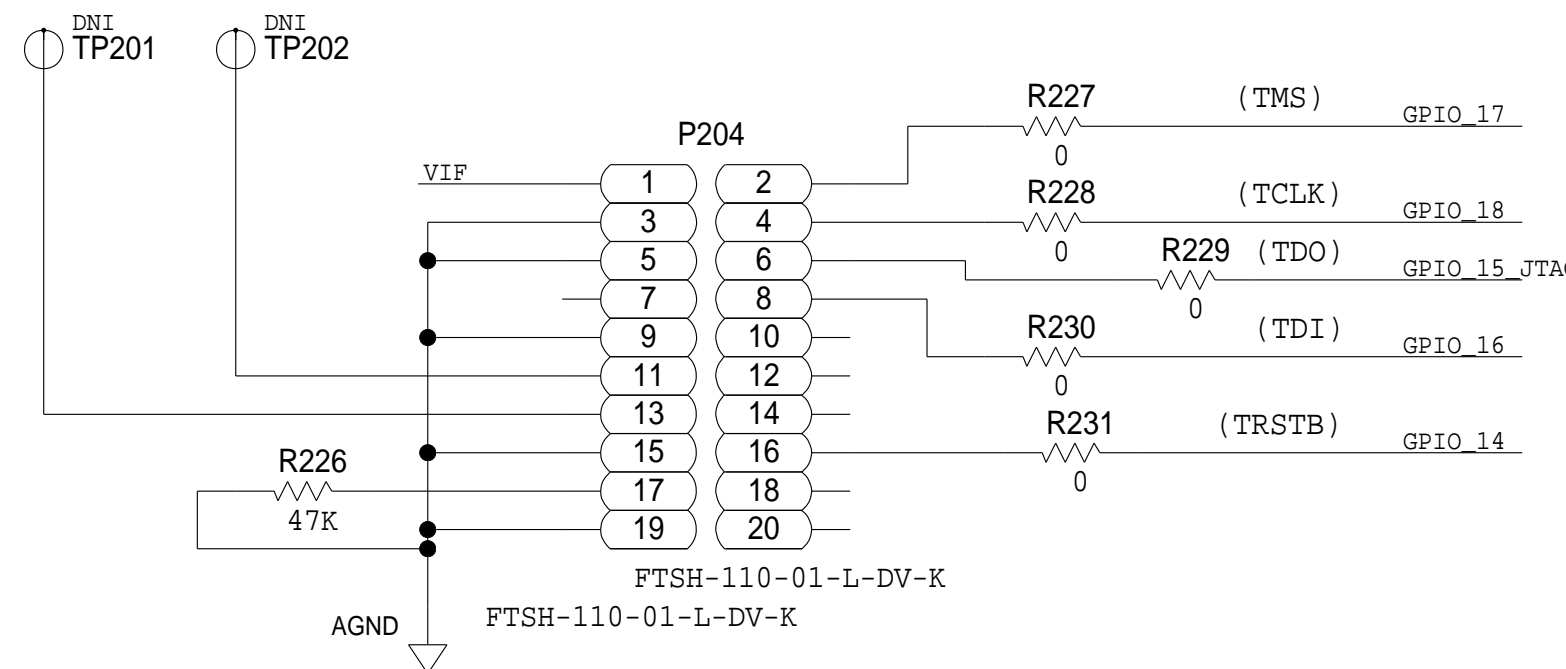
SPI INTERFACE BUFFER



BOARD ID EEPROM



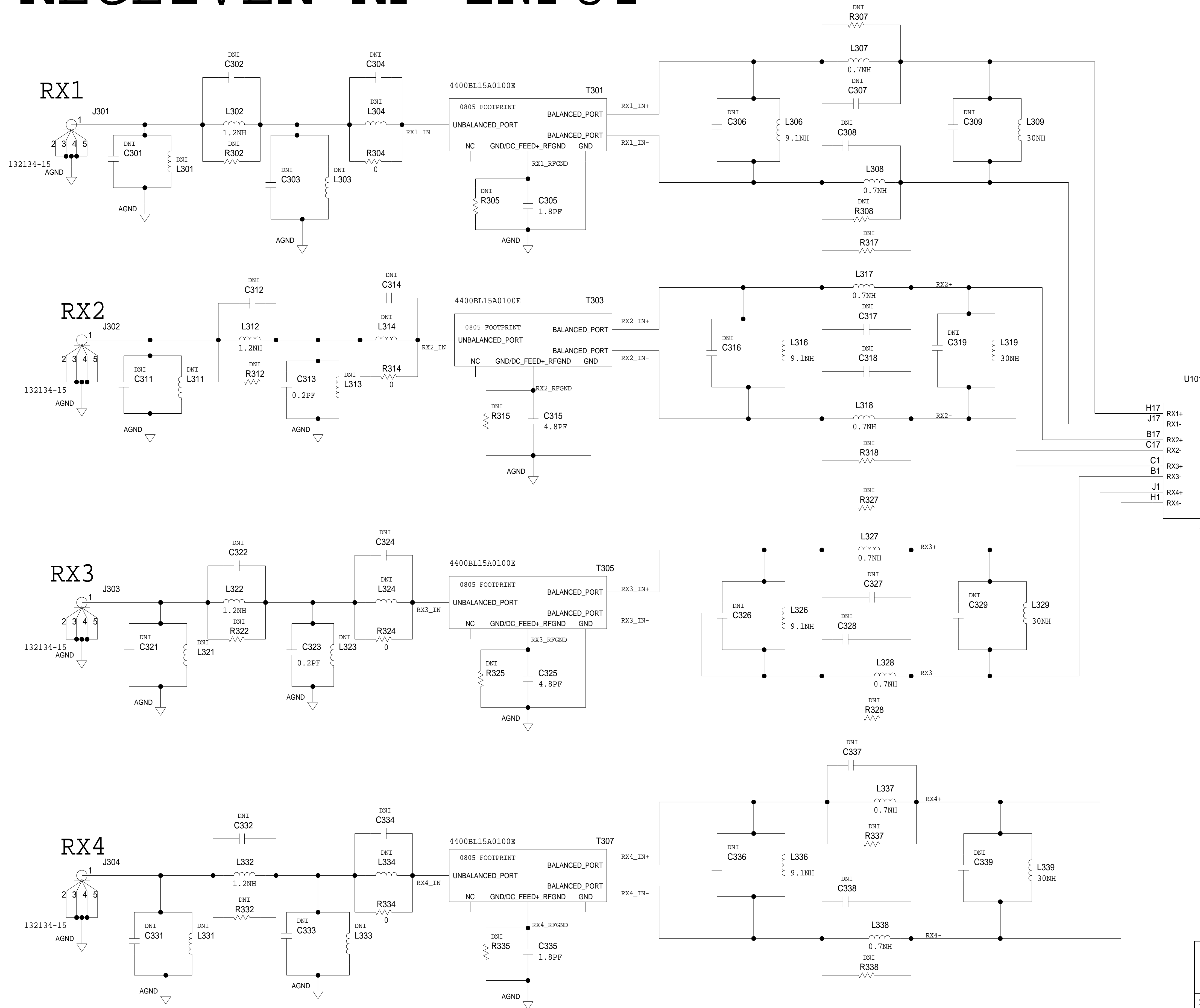
JTAG INTERFACE



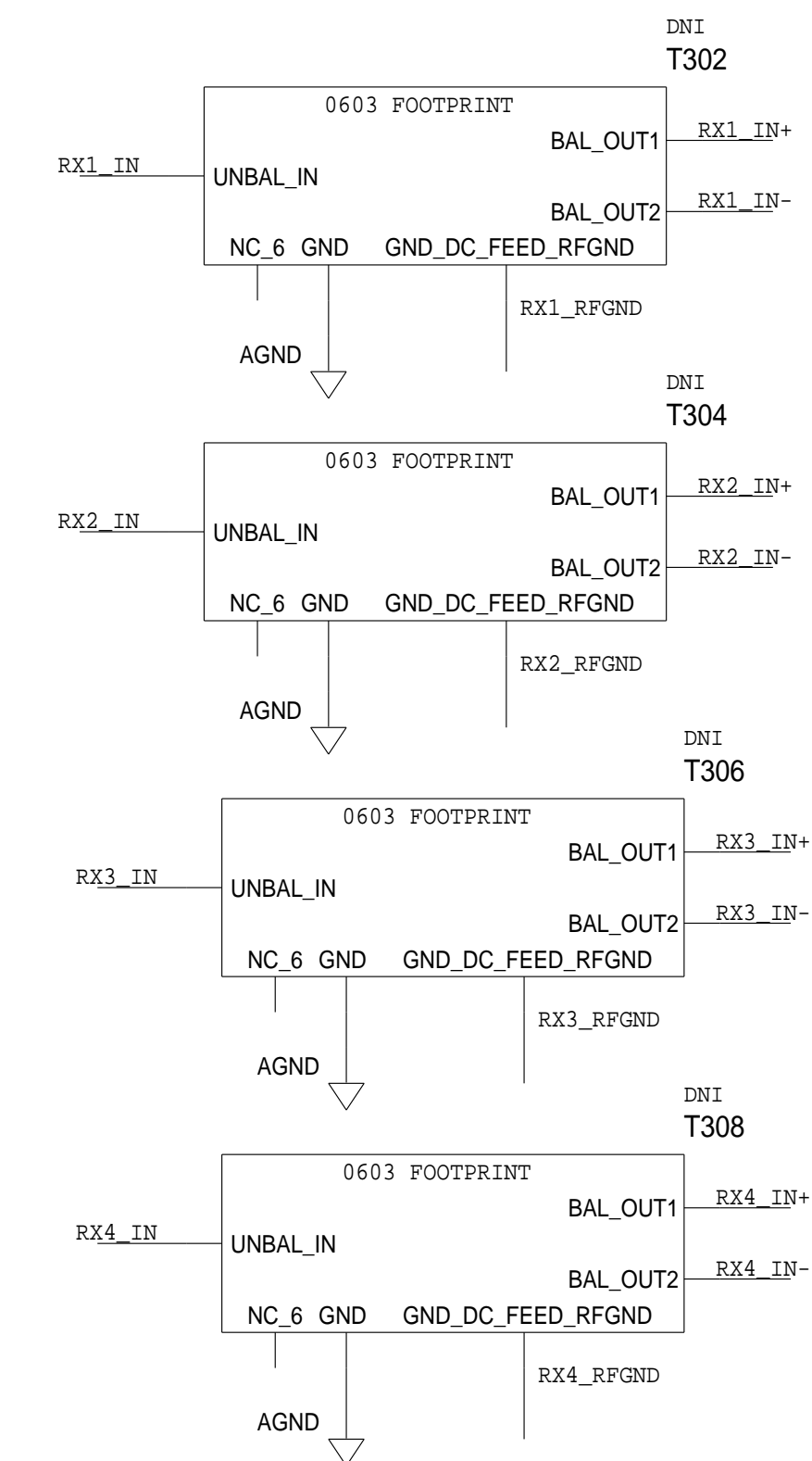
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

ANALOG DEVICES			
HW TYPE : Customer Evaluation Product(s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9026-HB			
DESIGN VIEW : ADRV9026-HB	DRAWING NO. 02_049168-01	REV C	
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 2 OF 10

RECEIVER RF INPUT



OVERLAP RX BALUNS

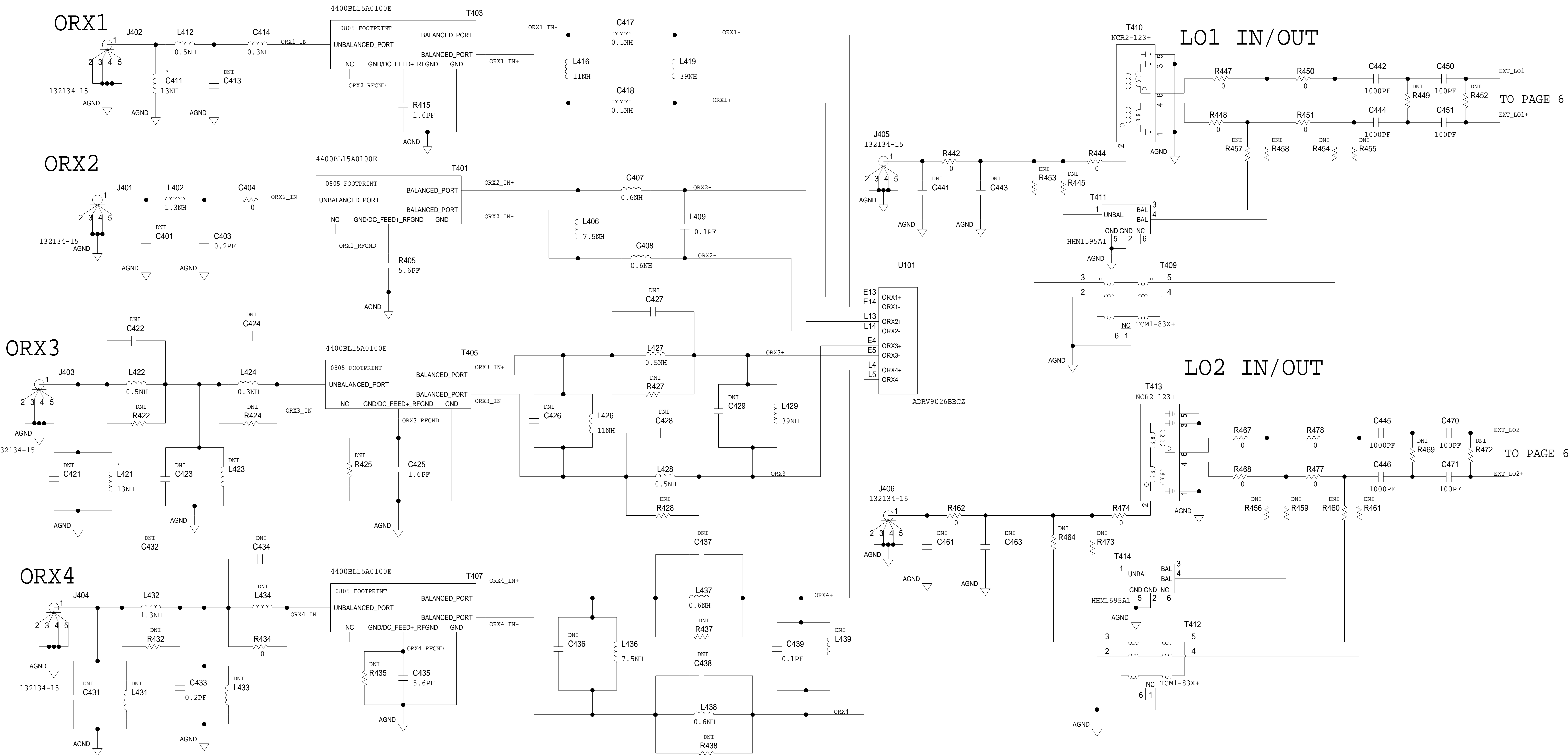


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

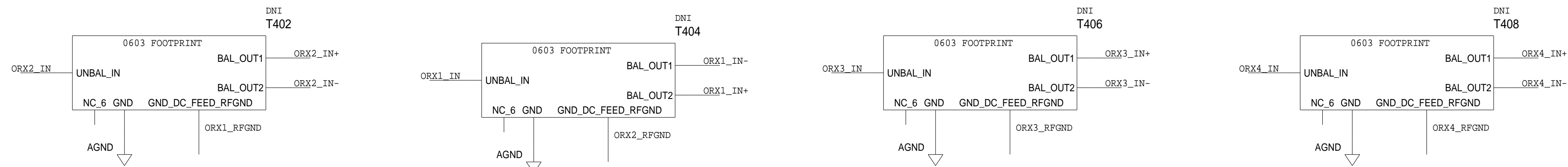
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9026-HB			
DESIGN VIEW : ADRV9026-HB	DRAWING NO. 02_049168-01	REV C	
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 3 OF 10

OBSERVATION RX RF INPUT

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



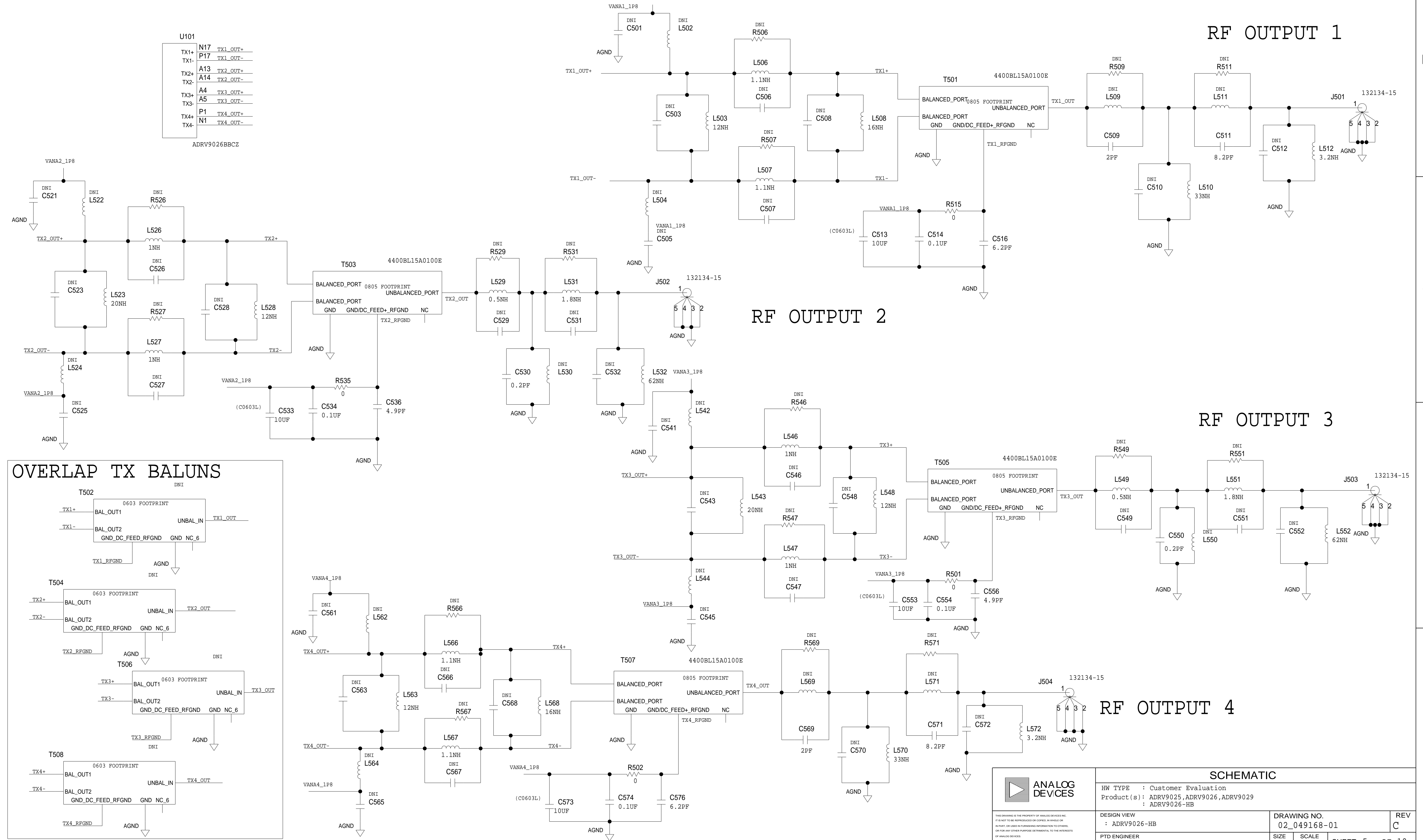
OVERLAP ORX BALUNS

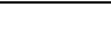


SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9026-HB			
DESIGN VIEW : ADRV9026-HB	DRAWING NO. 02_049168-01	REV C	
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 4 OF 10

TRANSMITTER RF OUTPUT

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

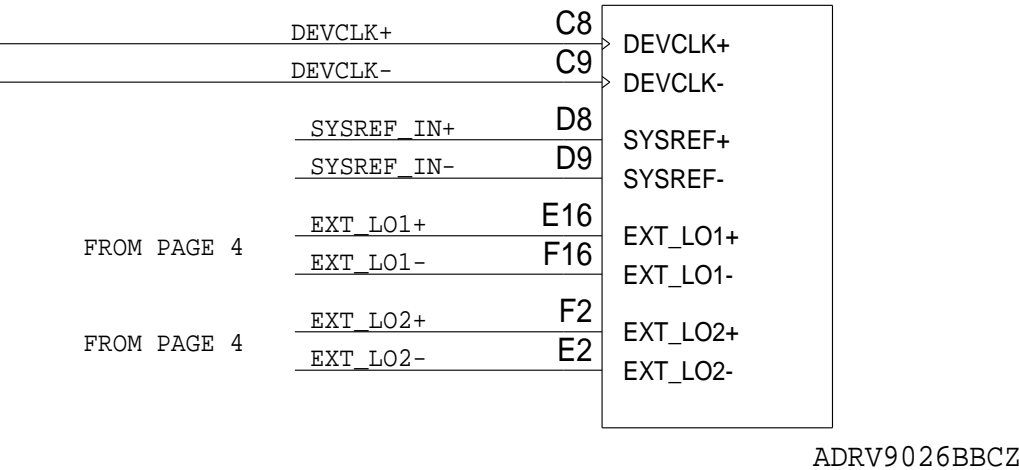
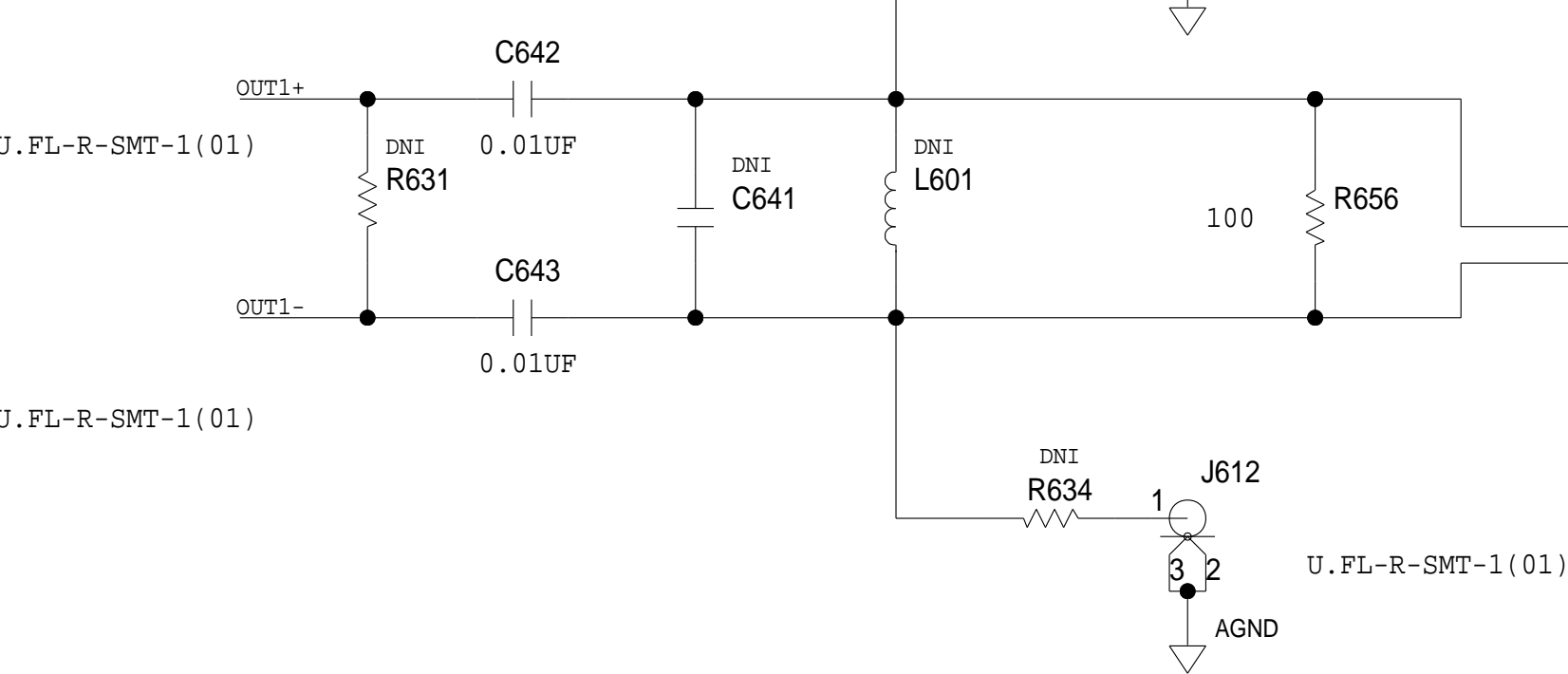
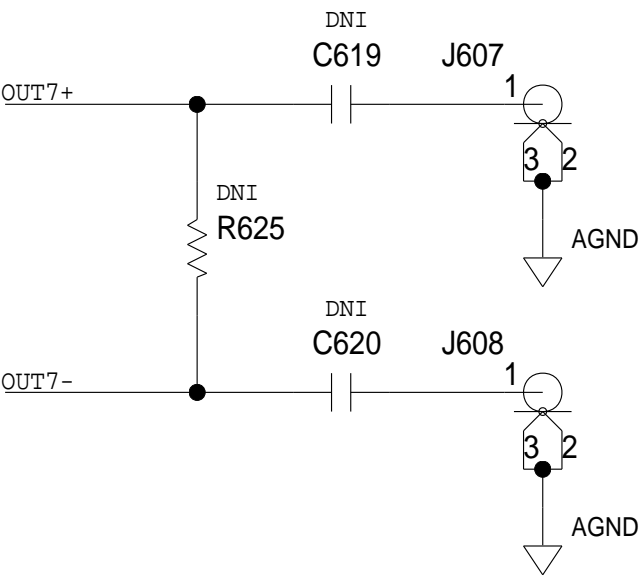
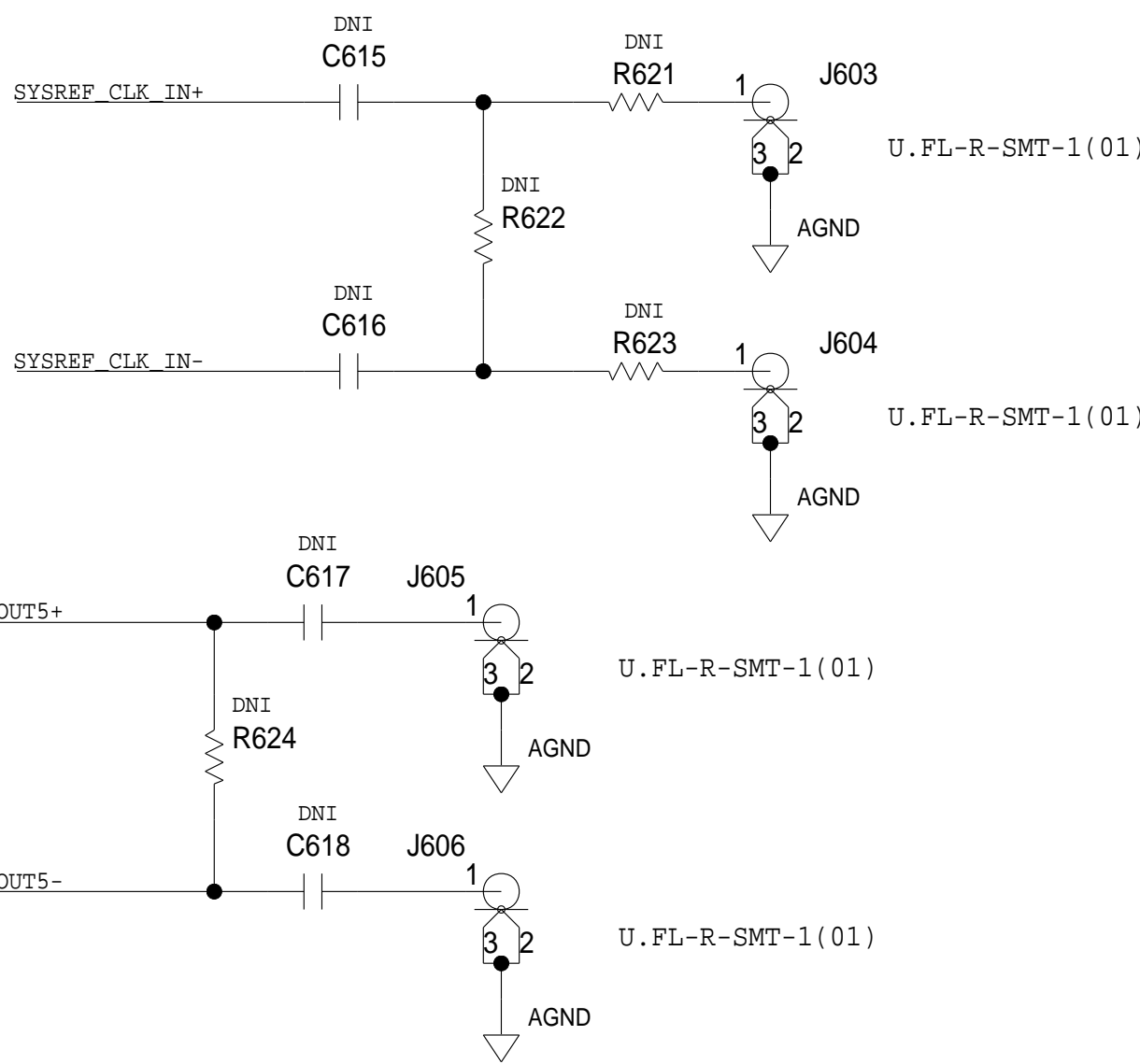
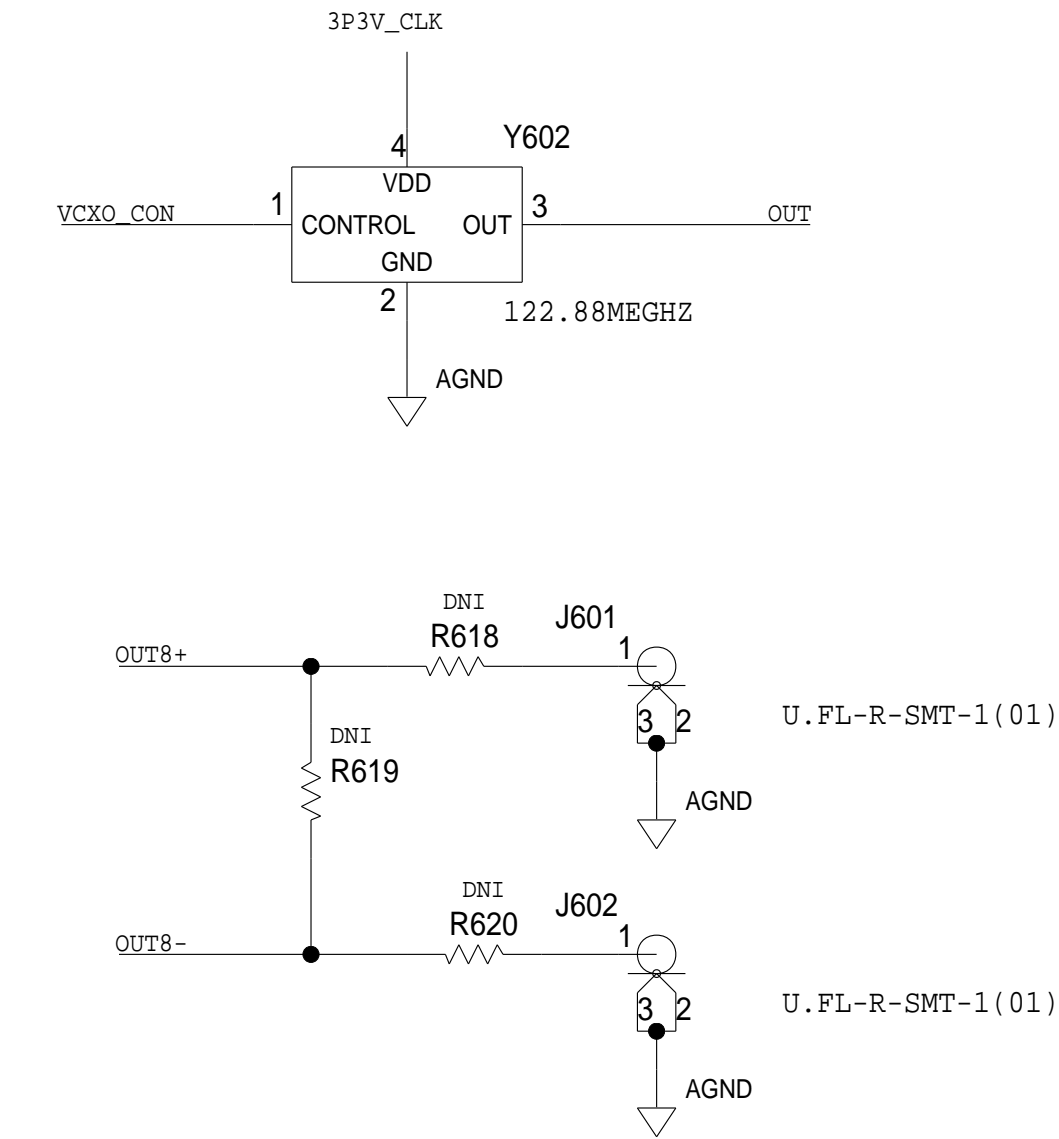
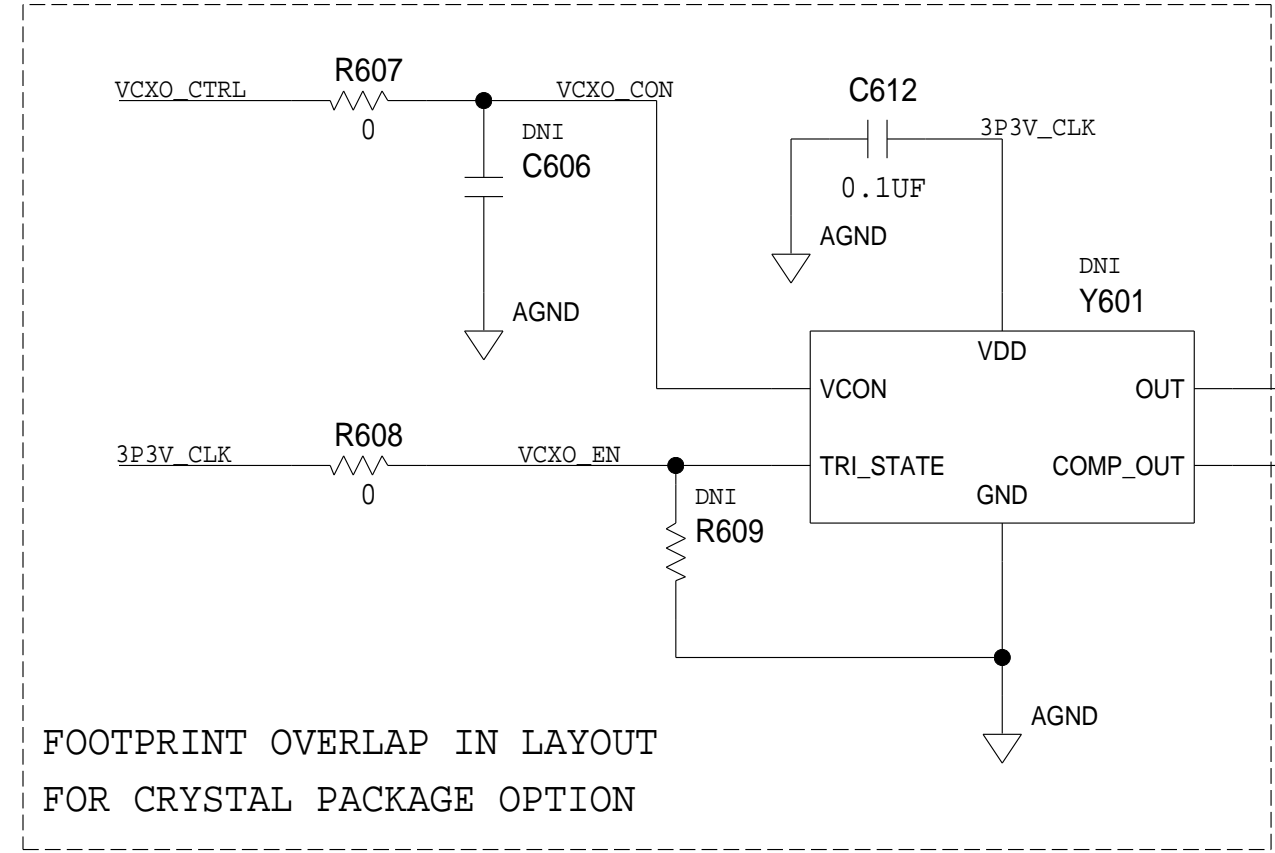


 ANALOG DEVICES	<h1 style="text-align: center;">SCHEMATIC</h1>			
	HW TYPE : Customer Evaluation Product(s) : ADRV9025, ADRV9026, ADRV9029 : ADRV9026-HB			
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PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 5	OF 10

CLOCK DISTRIBUTION

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

DEVICE CLK



SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9026-HB			
DESIGN VIEW : ADRV9026-HB	DRAWING NO. 02_049168-01	REV C	
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 6 OF 10

8 7 6 5 4 3 2 1

POWER INPUT

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

VCONV1_1P8
C701 10UF
C702 1UF
C703 0.1UF
AGND

VCONV2_1P8
C704 10UF
C705 1UF
C706 0.1UF
AGND

VANA1_1P8
C707 10UF
C708 1UF
C709 0.1UF
AGND

VANA2_1P8
C710 10UF
C711 1UF
C712 0.1UF
AGND

VANA3_1P8
C713 10UF
C714 1UF
C715 0.1UF
AGND

VANA4_1P8
C716 10UF
C717 1UF
C718 0.1UF
AGND

VJVC0_1P8
C719 10UF
C720 1UF
C721 0.1UF
AGND

VIF
C722 10UF
C723 1UF
C724 0.1UF
AGND

VANA1_1P3
C725 10UF
C726 1UF
C727 0.1UF
AGND

VANA2_1P3
C728 10UF
C729 1UF
C730 0.1UF
AGND

VCONV1_1P3
C731 10UF
C732 1UF
C733 0.1UF
AGND

VCONV2_1P3
C734 10UF
C735 1UF
C736 0.1UF
AGND

VRFVCO1_1P3
C737 10UF
C738 1UF
C739 0.1UF
AGND

VRFVCO2_1P3
C740 10UF
C741 1UF
C742 0.1UF
AGND

VAUXVCO_1P3
C743 10UF
C744 1UF
C745 0.1UF
AGND

VCLKVCO_1P3
C746 10UF
C747 1UF
C748 0.1UF
AGND

VRFSYN1_1P3
C749 10UF
C750 1UF
C751 0.1UF
AGND

VRFSYN2_1P3
C752 10UF
C753 1UF
C754 0.1UF
AGND

VCLKSYN_1P3
C755 10UF
C756 1UF
C757 0.1UF
AGND

VRXLO_1P3
C758 10UF
C759 1UF
C760 0.1UF
AGND

VTXLO_1P3
C761 10UF
C762 1UF
C763 0.1UF
AGND

VDES_1P0
C777 10UF
C778 0.1UF
C779 1UF
AGND

VSER_1P0
C780 10UF
C781 0.1UF
C782 1UF
AGND

VTT_DES
C783 10UF
C784 0.1UF
C785 1UF
AGND

VJSYN_1P0
C758 10UF
C759 1UF
C760 0.1UF
AGND

U101
VSSA
VCONV1_1P8
VCONV2_1P8
VANA1_1P8
VANA2_1P8
VANA3_1P8
VANA4_1P8
VJVC0_1P8
VIF
VANA1_1P3
VANA2_1P3
VCONV1_1P3
VCONV2_1P3
VRFVCO1_1P3
VRFVCO2_1P3
VAUXVCO_1P3
VCLKVCO_1P3
VRFSYN1_1P3
VRFSYN2_1P3
VCLKSYN_1P3
VAUXSYN_1P3
VRXLO_1P3
VTXLO_1P3
VJSYN_1P0
VDES_1P0
VTT_DES
VSER_1P0
VSSD
VDIG_1P0
VDIG_1P0
VDIG_1P0
VDIG_1P0
VSSA/DDQ
AGND

R701 0
R702 0
R703 0
R704 0
R707 DNI
C770 4.7UF
C771 4.7UF
C772 4.7UF
C773 4.7UF
C774 4.7UF
C775 4.7UF
C776 DNI

VDIG_1P0
C786 100UF
C787 100UF
C788 0.1UF
C789 0.1UF
C790 0.1UF
C791 1UF
AGND

ADRV9026BBCZ

ANALOG DEVICES

HW TYPE : Customer Evaluation
Product(s) : ADRV9025, ADRV9026, ADRV9029
: ADRV9026-HB

DESIGN VIEW : ADRV9026-HB

PTD ENGINEER R. MYERS


DRAWING NO. 02_049168-01

REV C

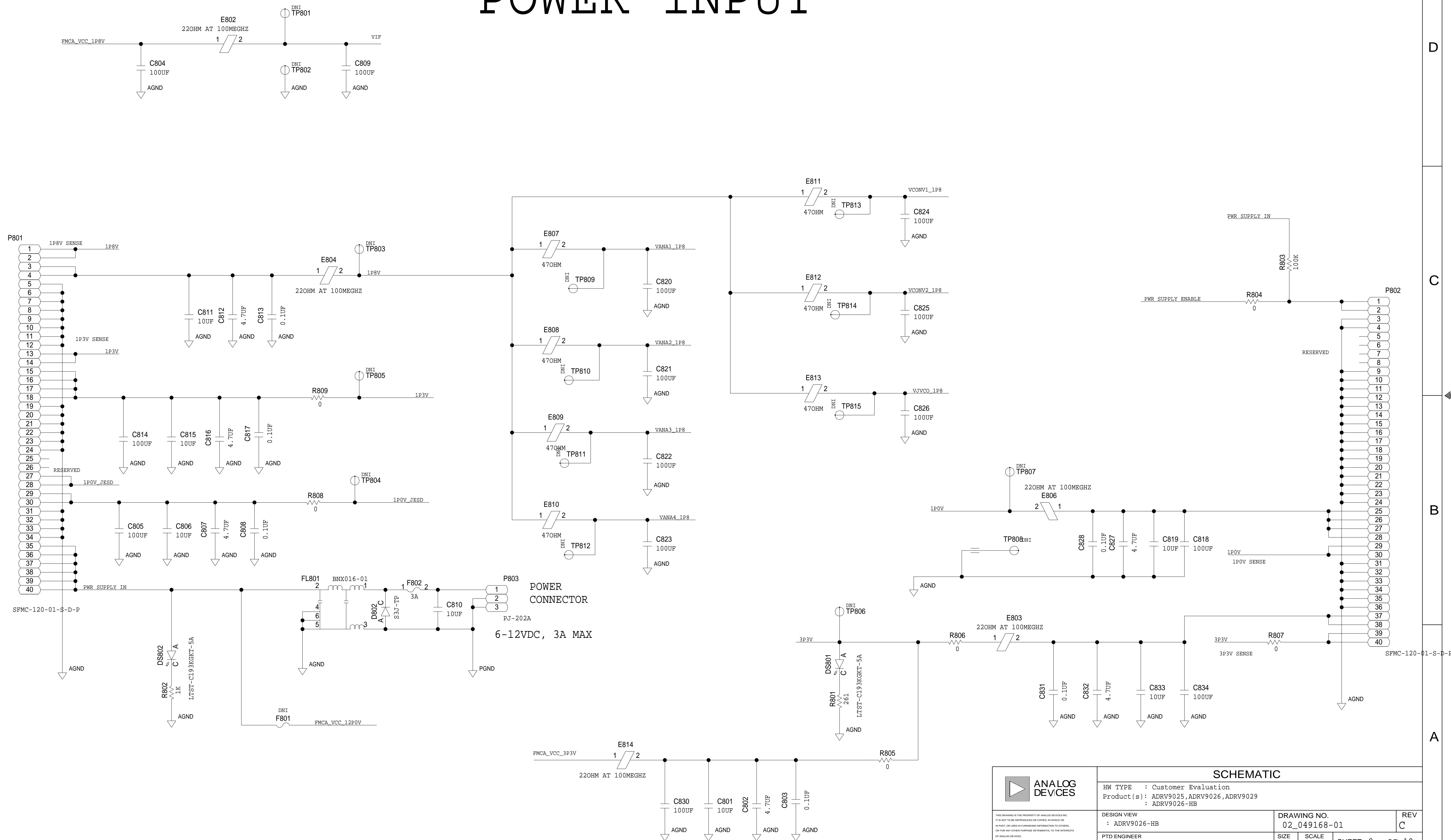
SIZE D SCALE 1:1


SHEET 7 OF 10

8 7 6 5 4 3 2 1

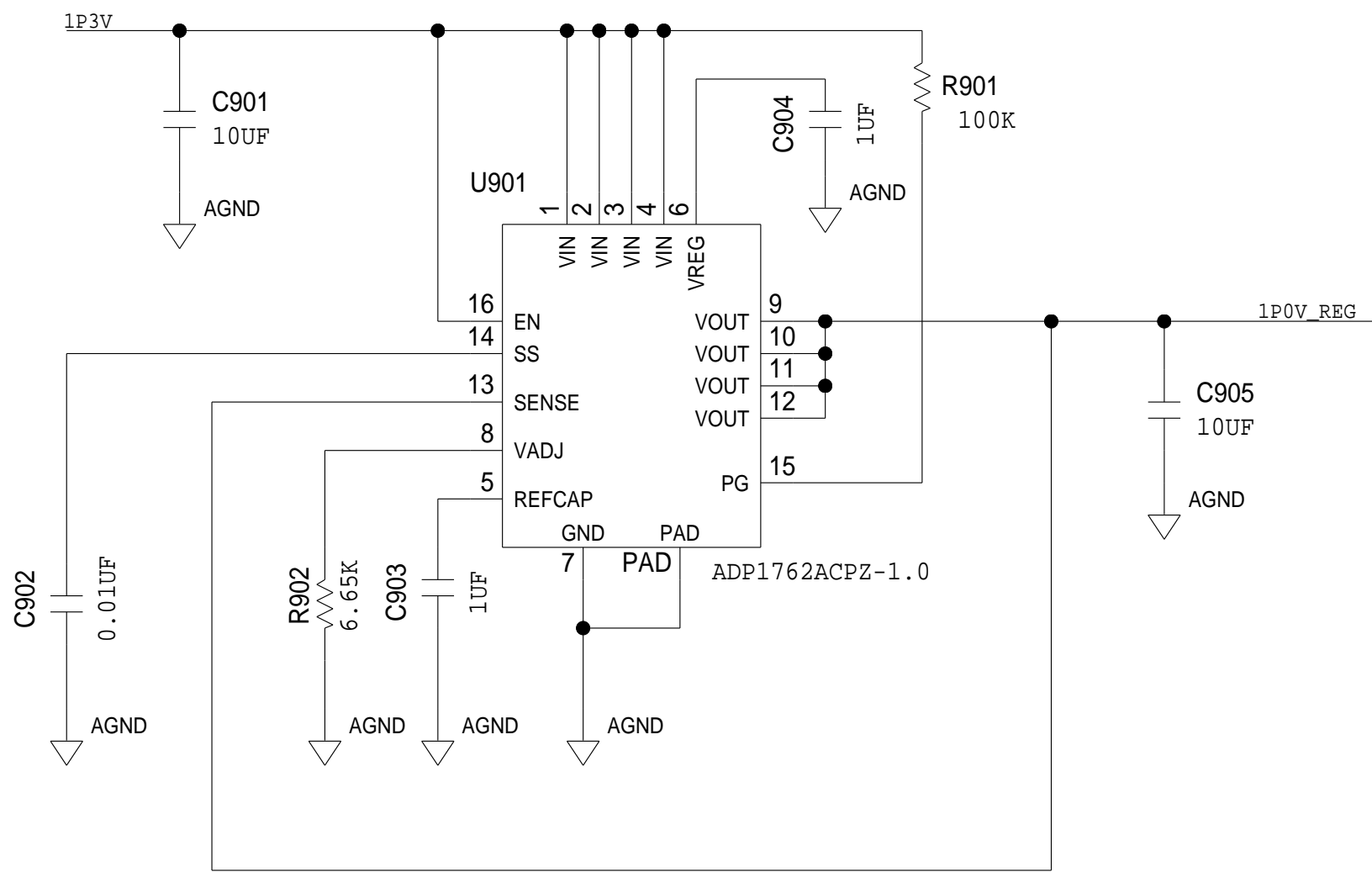
 ANALOG DEVICES	<h1 style="margin: 0;">SCHEMATIC</h1>			
	<p>HW TYPE : Customer Evaluation</p> <p>Product(s) : ADRV9025, ADRV9026, ADRV9029</p> <p style="padding-left: 40px;">: ADRV9026-HB</p>			
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PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 7	OF 10

POWER INPUT

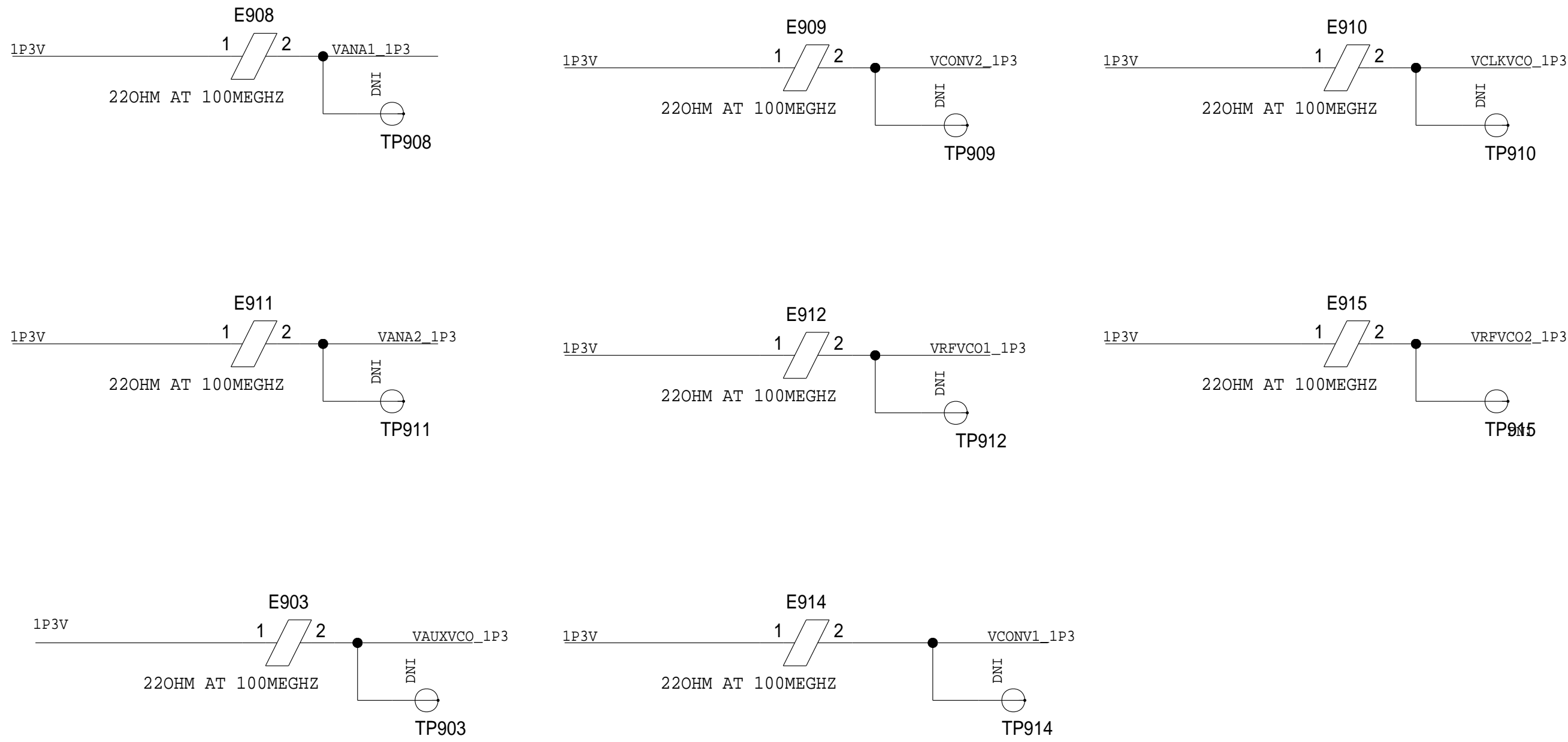


 ANALOG DEVICES	<h1 style="text-align: center;">SCHEMATIC</h1>				
	<p>HW TYPE : Customer Evaluation</p> <p>Product (s) : ADRV9025, ADRV9026, ADRV9029</p> <p style="padding-left: 100px;">: ADRV9026-HB</p>				
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	<p>PTD ENGINEER</p> <p>R. MYERS</p>	<p>SHEET 8 OF 10</p>	<p>SIZE</p> <p style="text-align: center;">D</p>	<p>SCALE</p> <p style="text-align: center;">1:1</p>	

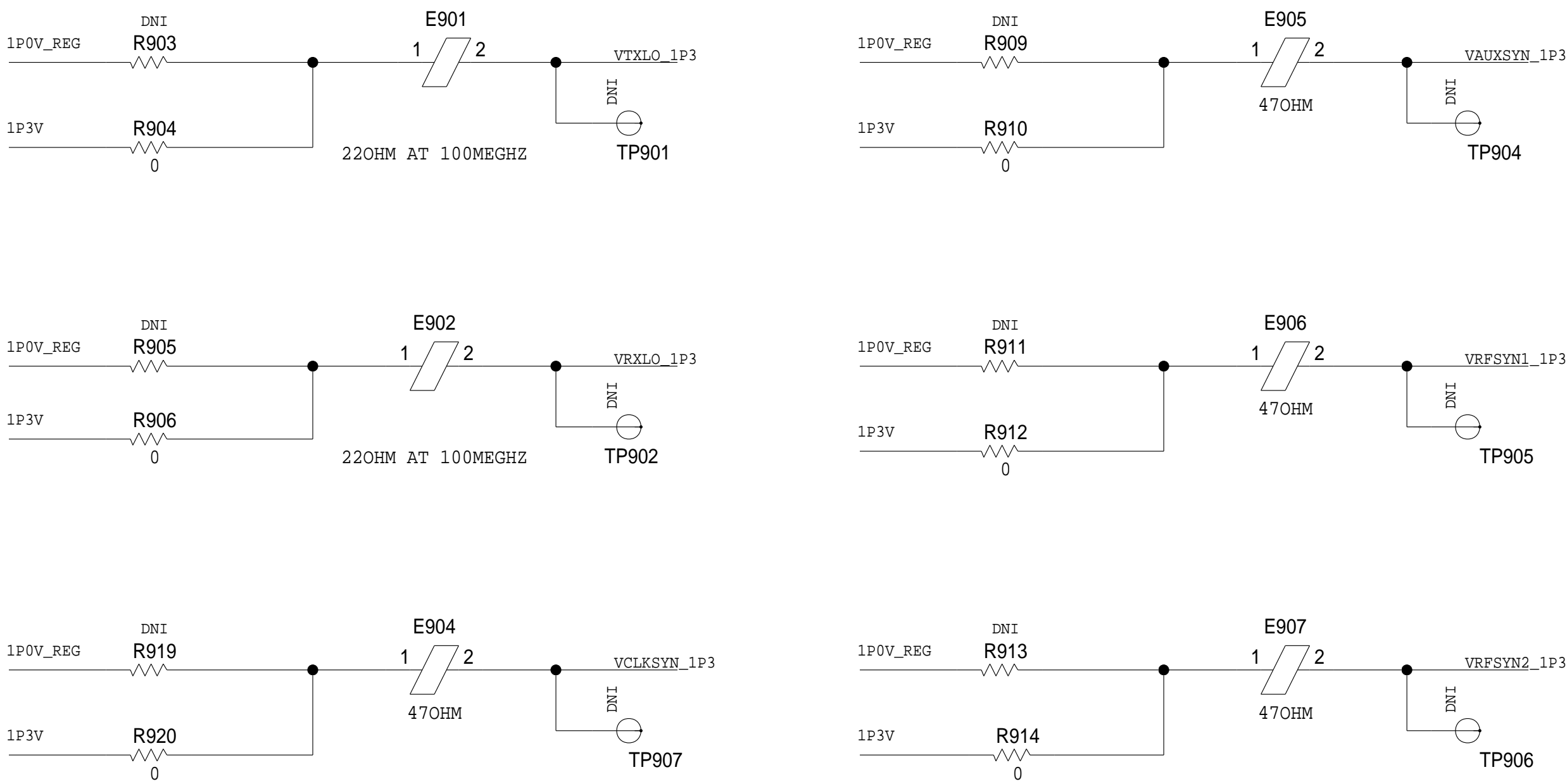
POWER DISTRIBUTION



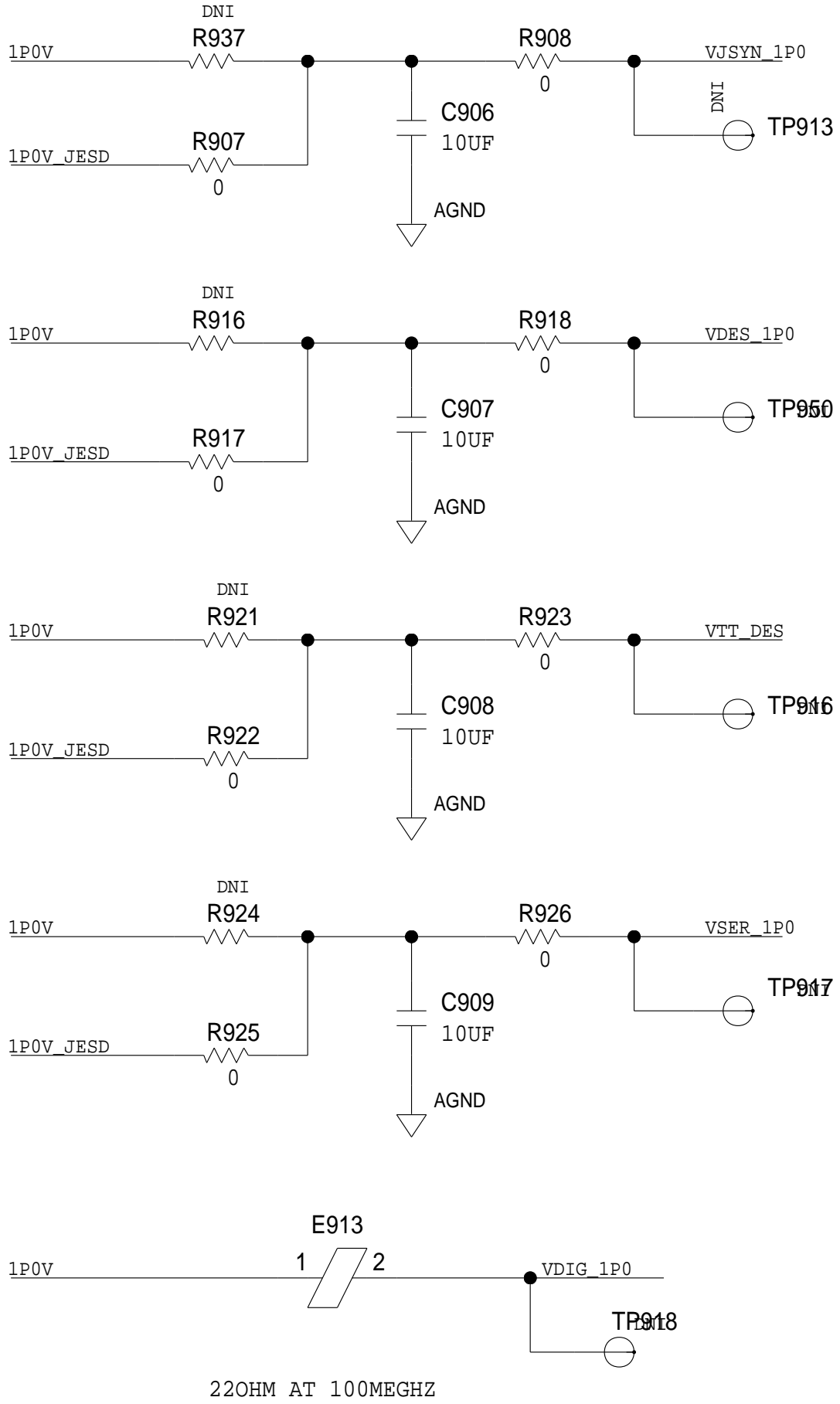
1.3V ANALOG SUPPLIES



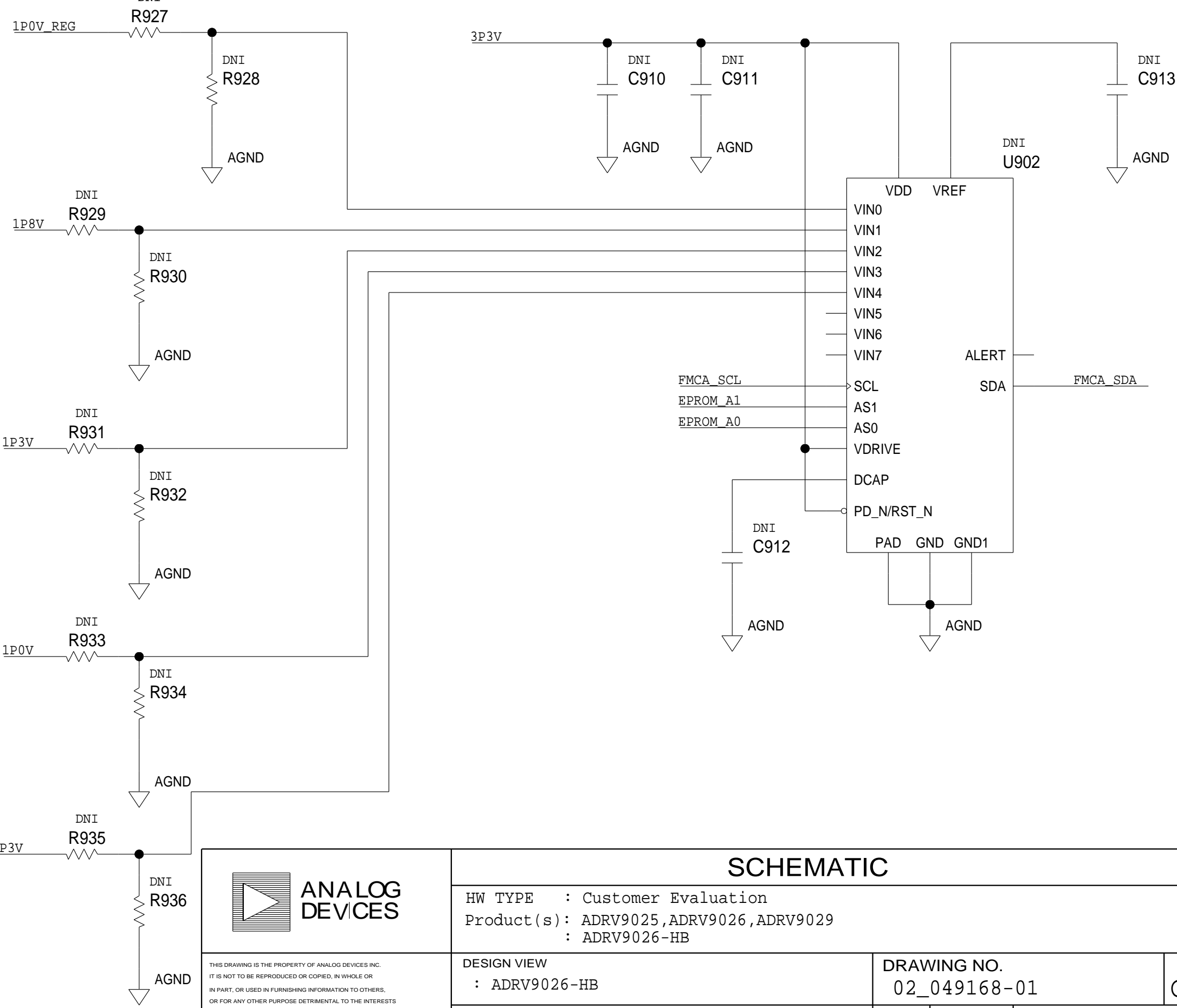
1.0V ANALOG SUPPLIES WITH 1.3V OPTION



1.0V DIGITAL SUPPLIES



SUPPLY TEST MONITOR

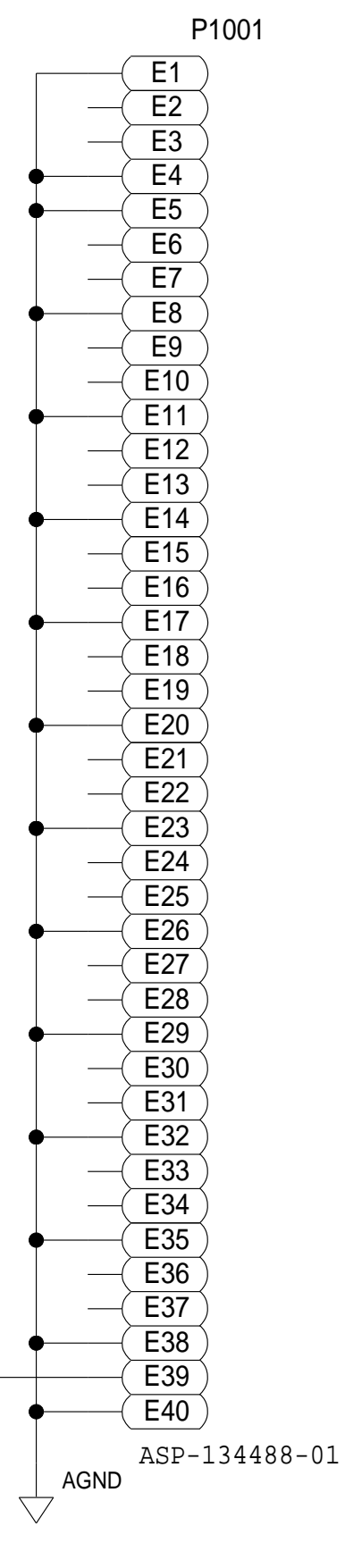
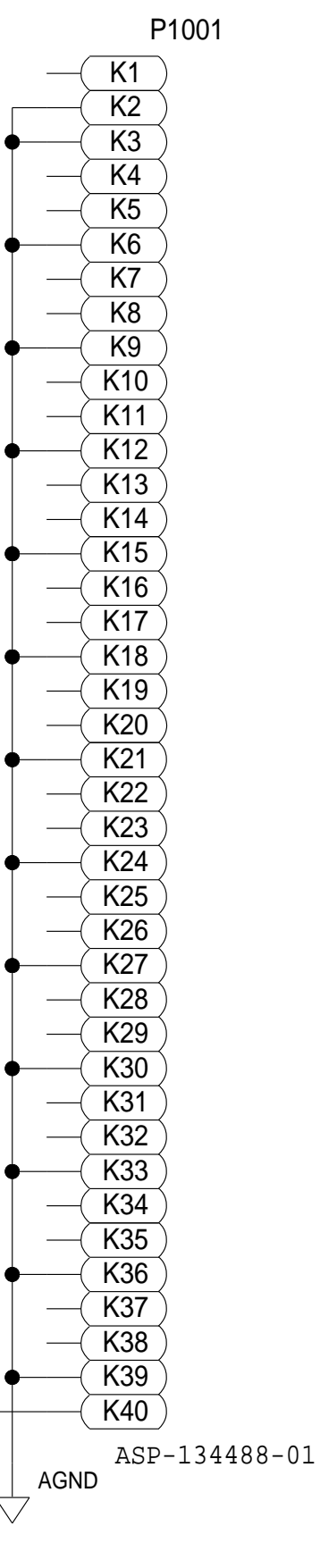
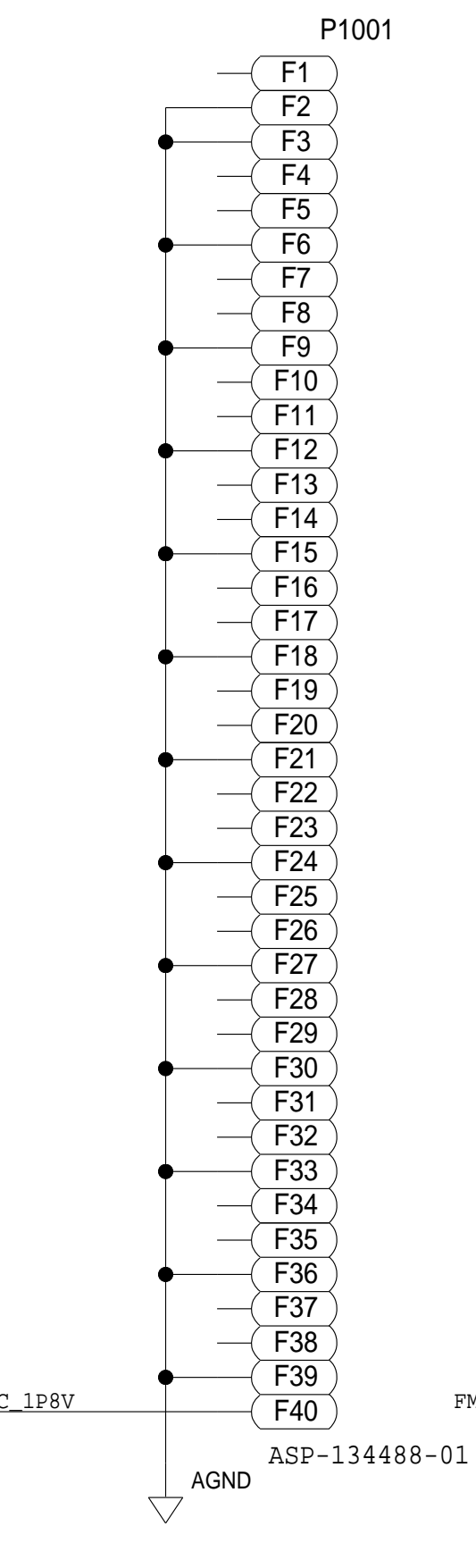
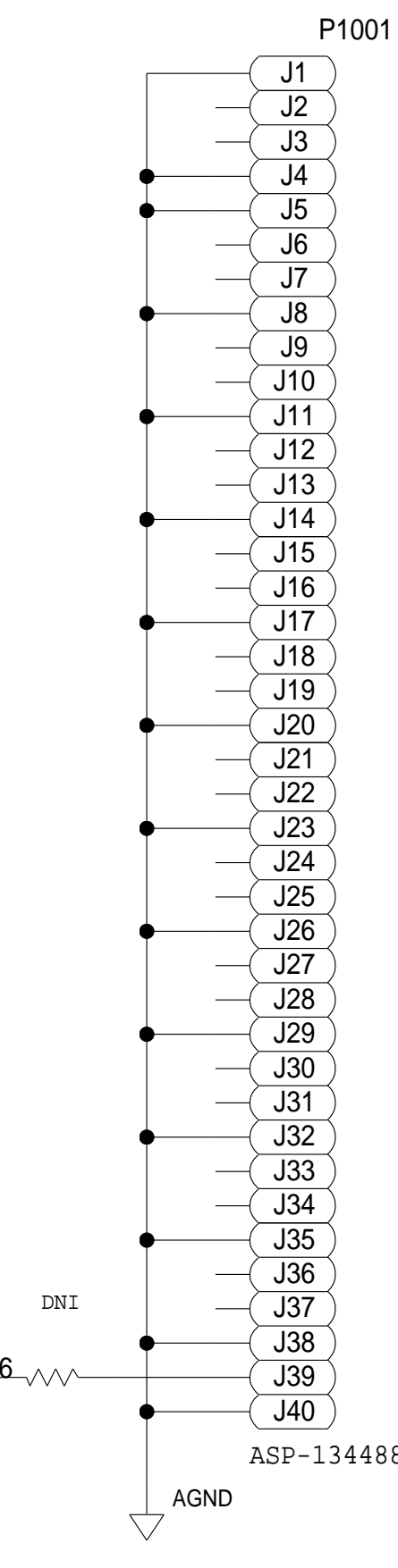
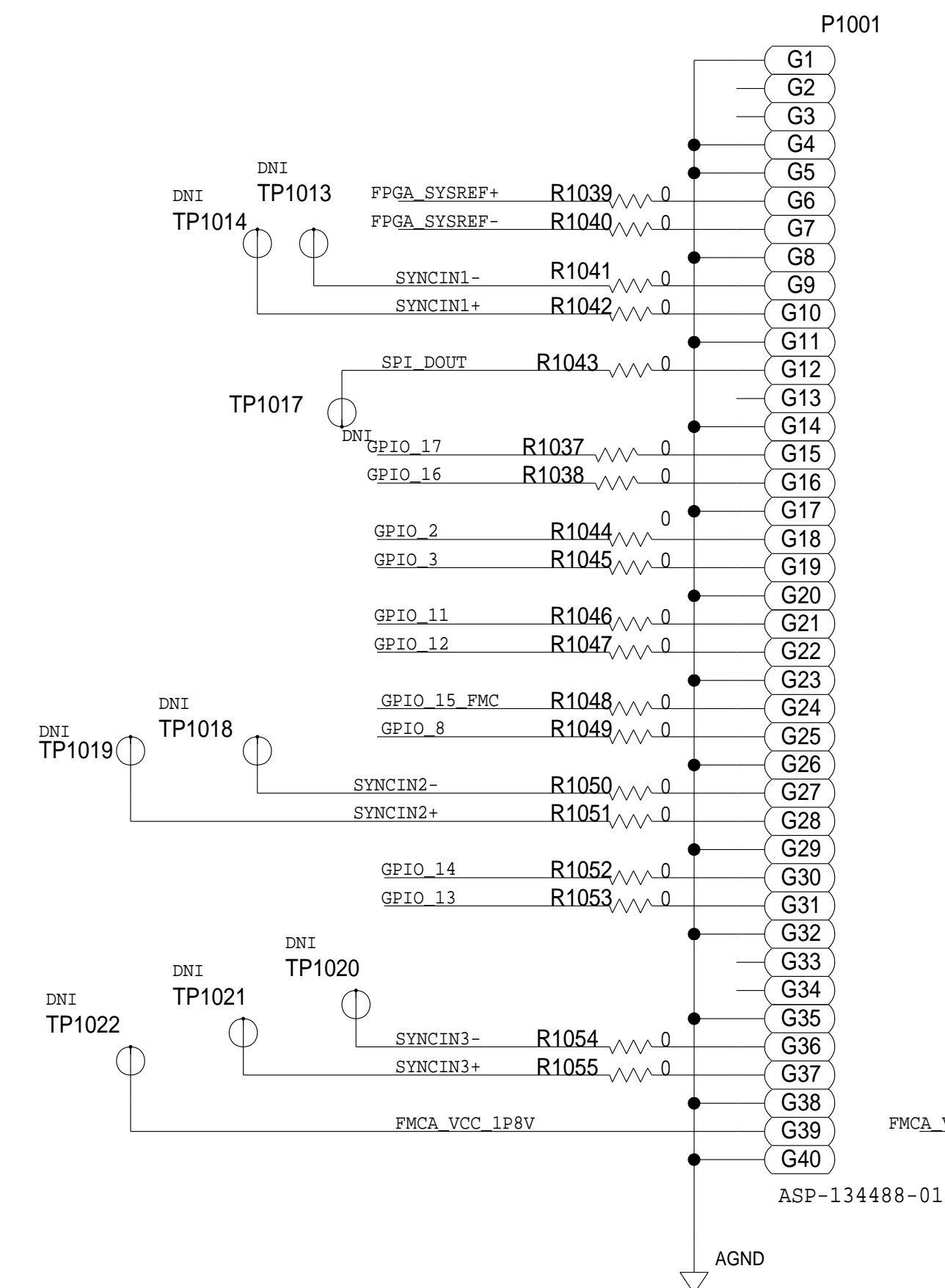
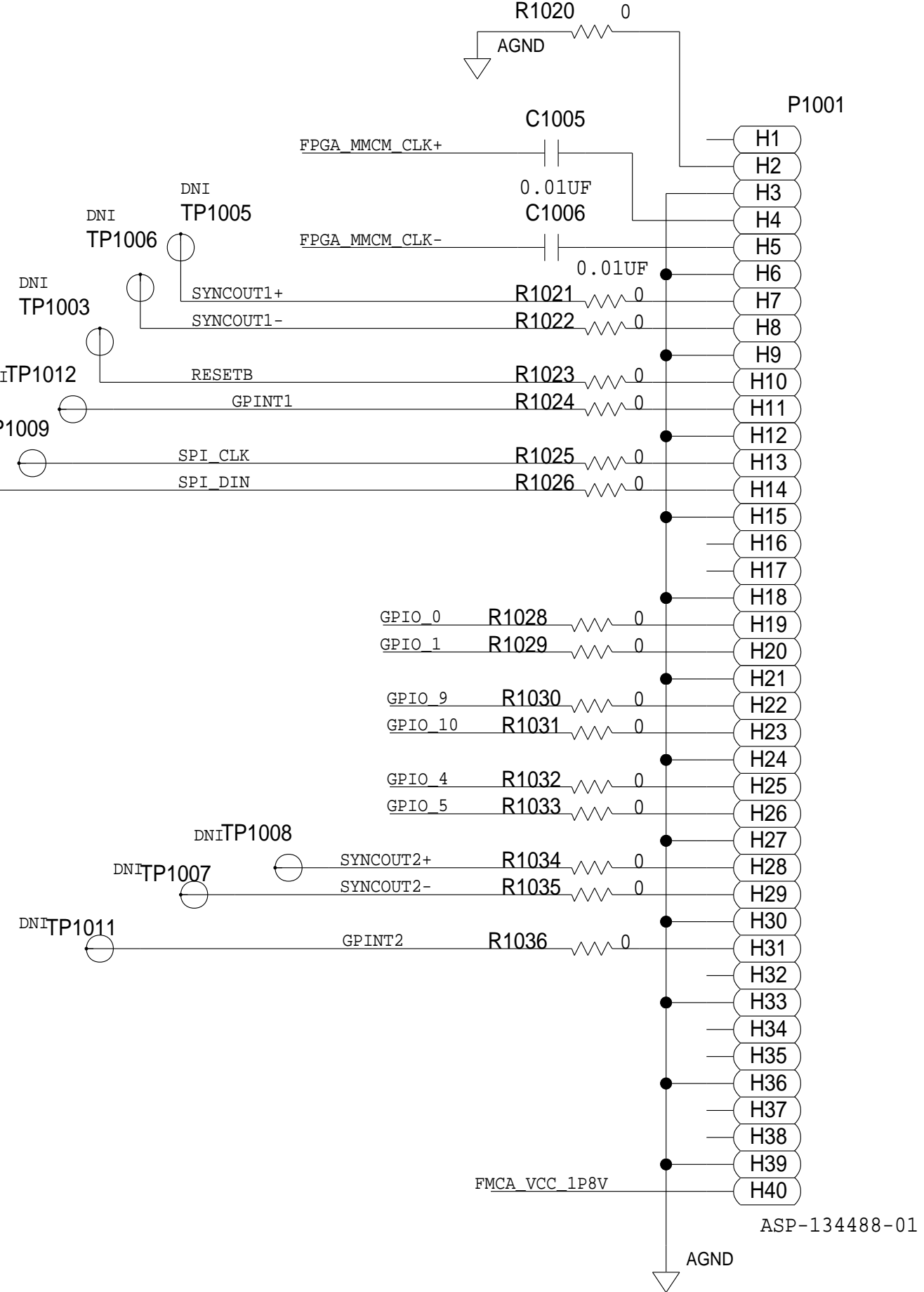
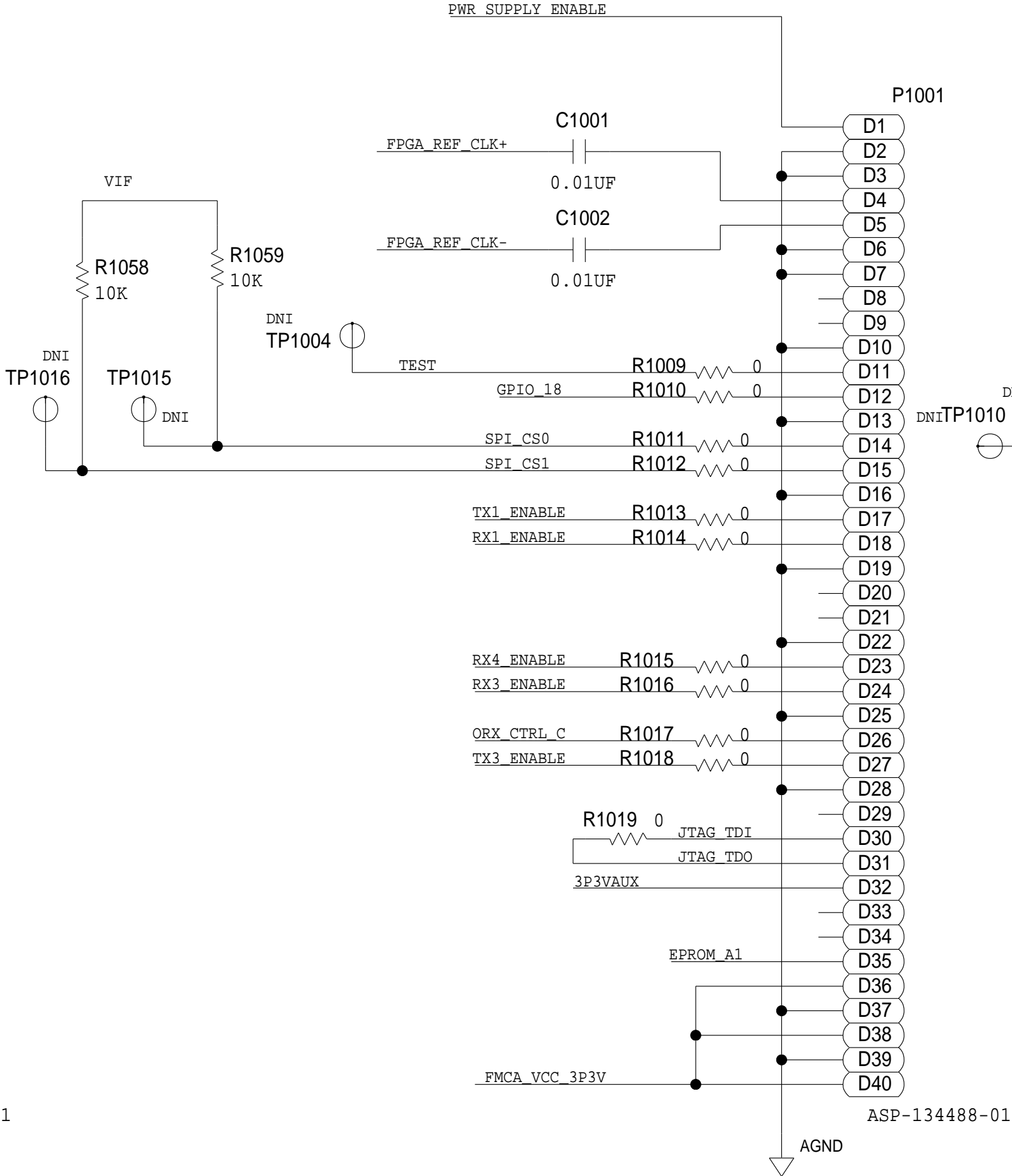
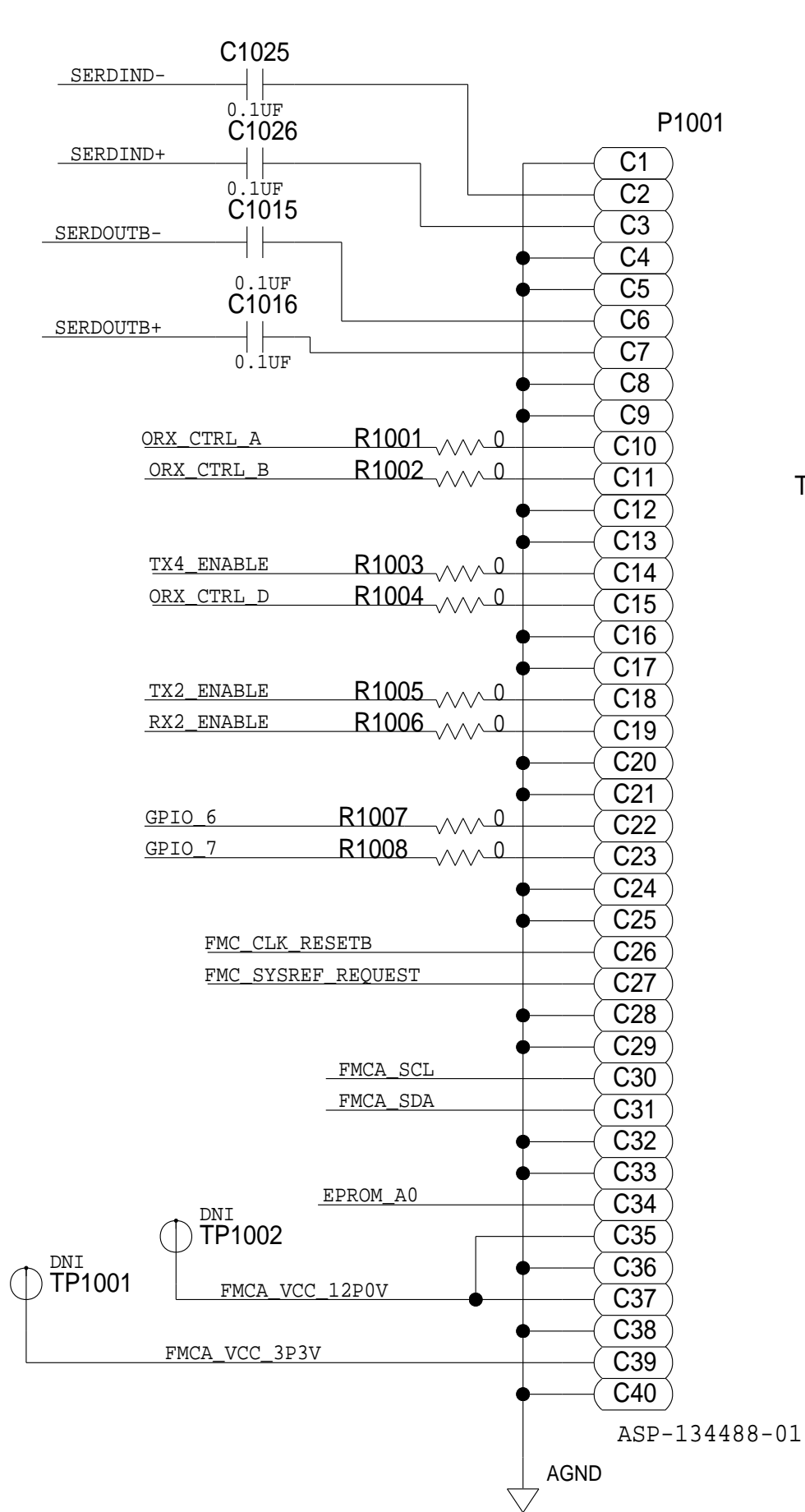
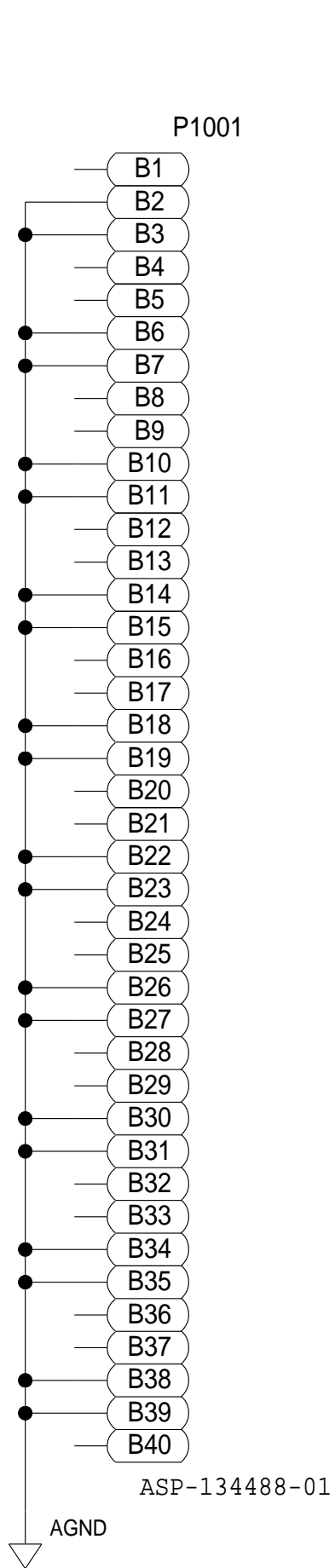
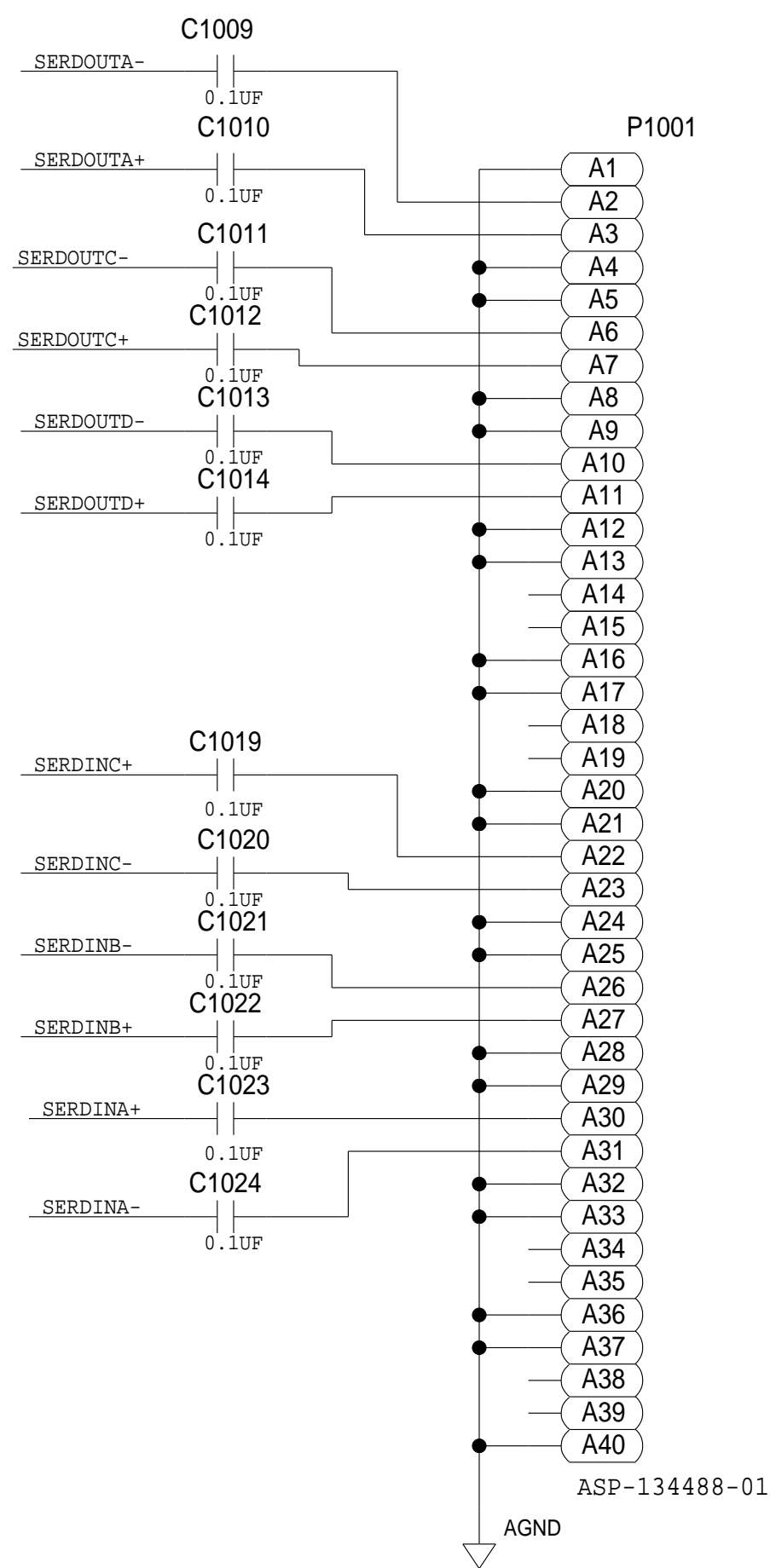


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

ANALOG DEVICES		SCHEMATIC	
HW TYPE : Customer Evaluation		Product (s) : ADRV9025,ADRV9026,ADRV9029	
: ADRV9026-HB		: ADRV9026-HB	
DESIGN VIEW : ADRV9026-HB		DRAWING NO. 02_049168-01	
PTD ENGINEER R. MYERS		REV C	
SIZE D		SCALE 1:1	
SHEET 9		OF 10	

FPGA CONNECTIONS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



U101			
SERDOUTA+	T7	SERDOUTA+	T11
SERDOUTA-	T8	SERDOUTA-	T10
SERDOUTB+	U5	SERDOUTB+	U12
SERDOUTB-	U6	SERDOUTB-	U13
SERDOUTC+	T3	SERDOUTC+	T15
SERDOUTC-	T4	SERDOUTC-	T14
SERDOUTD+	U1	SERDOUTD+	U16
SERDOUTD-	U2	SERDOUTD-	U17
SYNCOUT1+	P15	SYNCOUT1+	N12
SYNCOUT1-	R15	SYNCOUT1-	N13
SYNCOUT2+	N14	SYNCOUT2+	P7
SYNCOUT2-	N15	SYNCOUT2-	P8
		SYNCIN1+	N6
		SYNCIN1-	P6
		SYNCIN2+	
		SYNCIN2-	
		SYNCIN3+	
		SYNCIN3-	

ADRV9026BBCZ

SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADRV9025,ADRV9026,ADRV9029 : ADRV9026-HB			
DESIGN VIEW : ADRV9026-HB	DRAWING NO. 02_049168-01	REV C	
PTD ENGINEER R. MYERS	SIZE D	SCALE 1:1	SHEET 10 OF 10